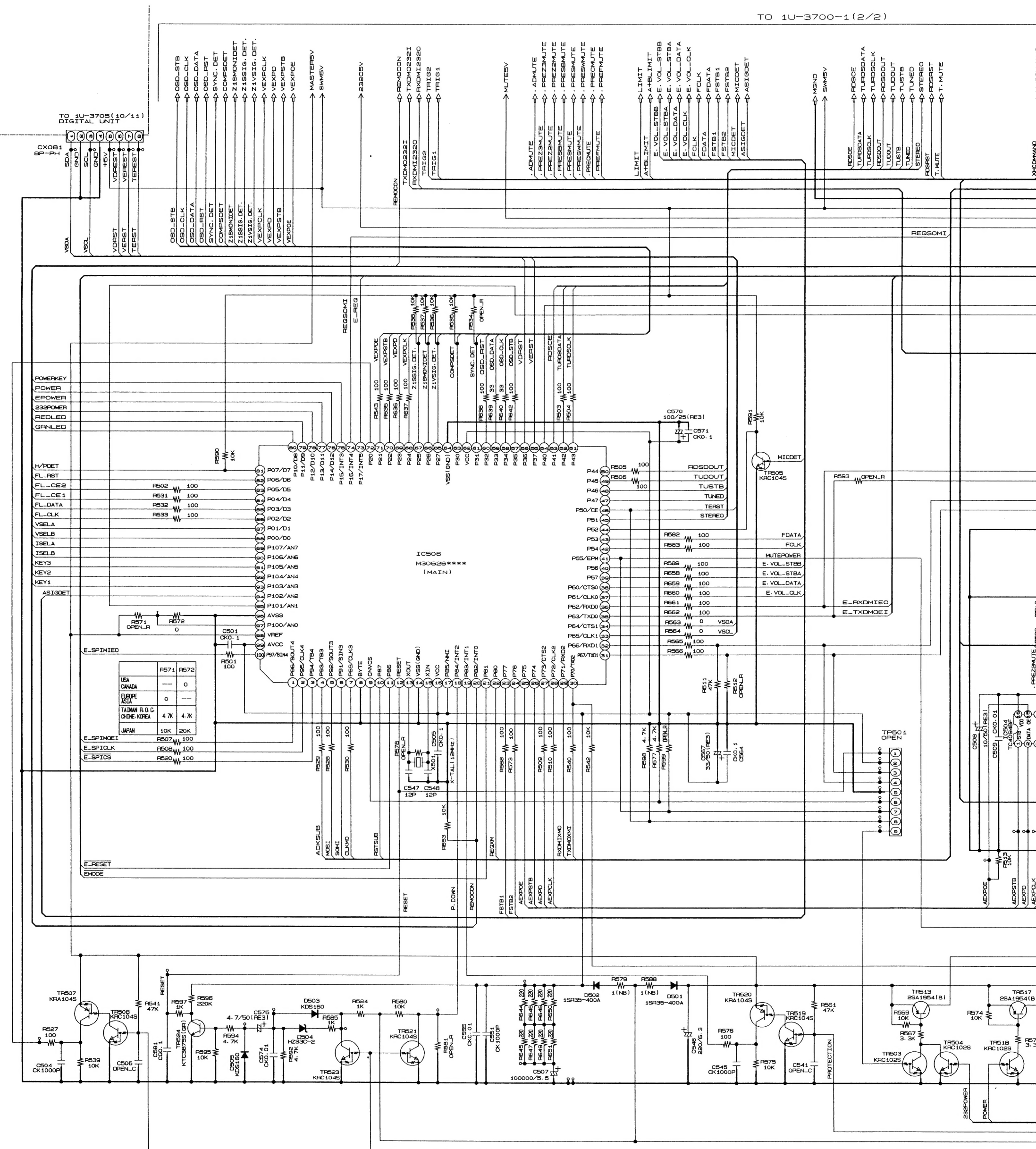


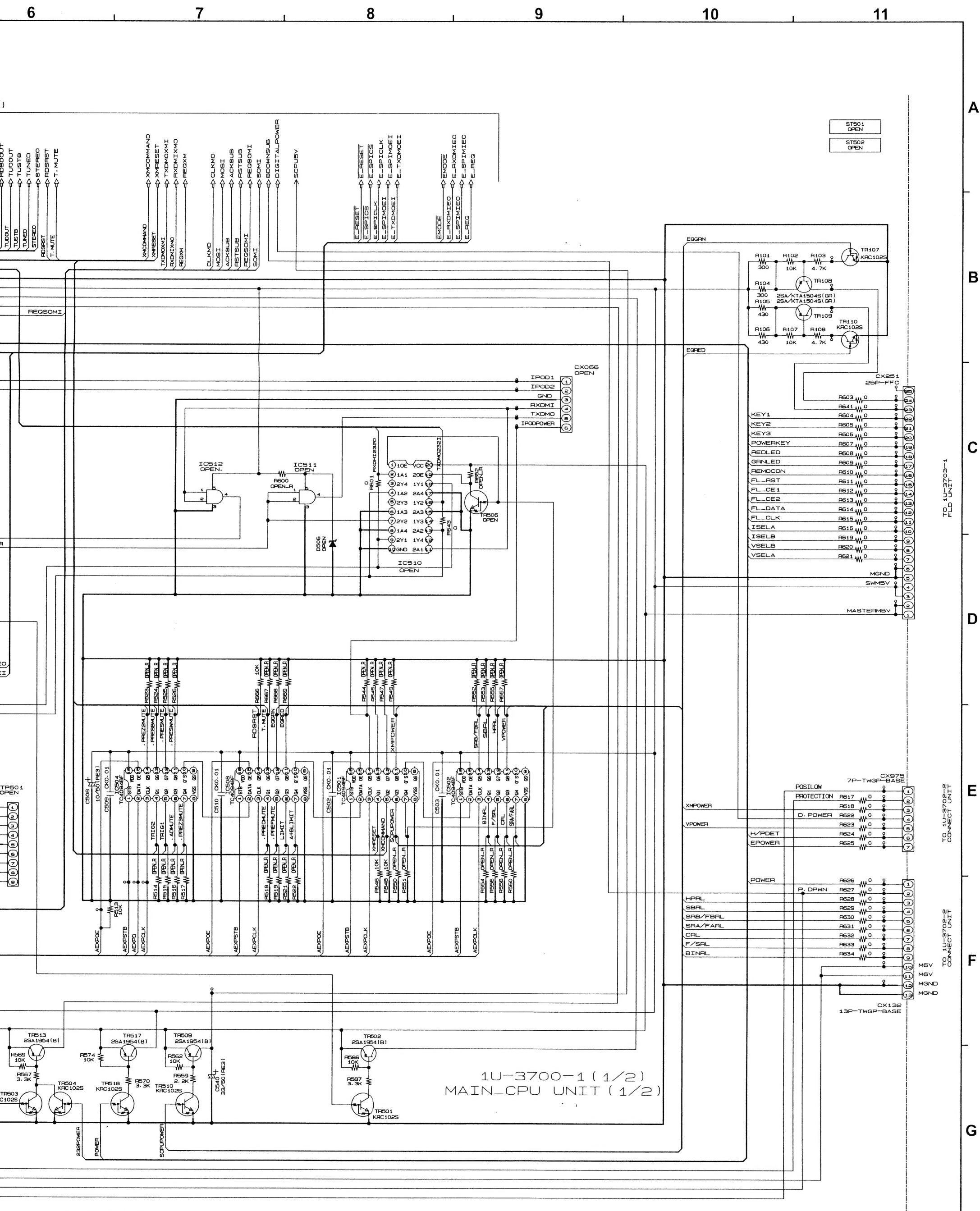
SCHALTBILD

DENON

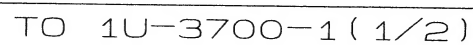
AVR3806

6

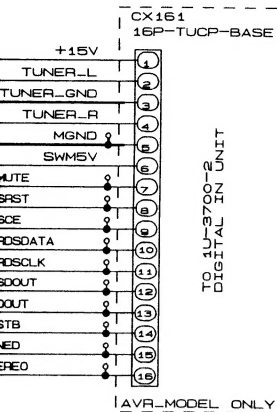




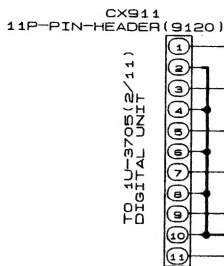
SCHEMATIC DIAGRAMS (1/31)
1U-3700-1 MAIN CPU UNIT (1/2)



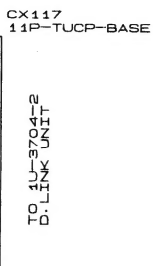
DIGITAL IN
1U-3700-2



TO DIGITAL UNIT
1U-3705

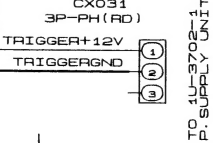
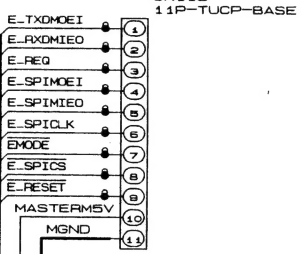
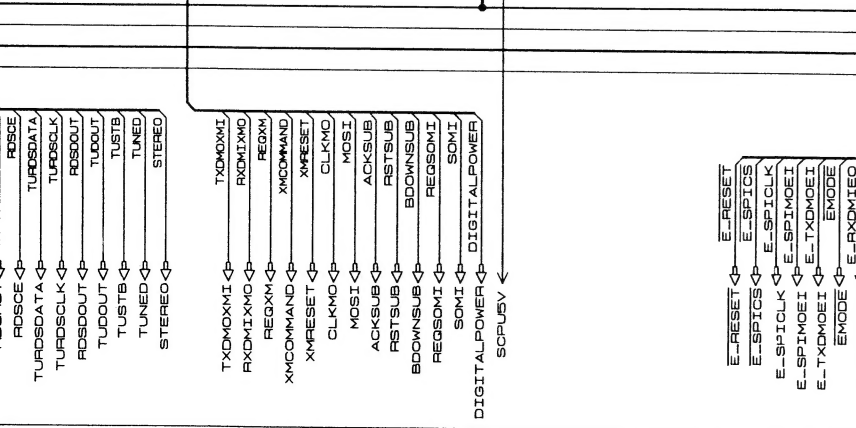
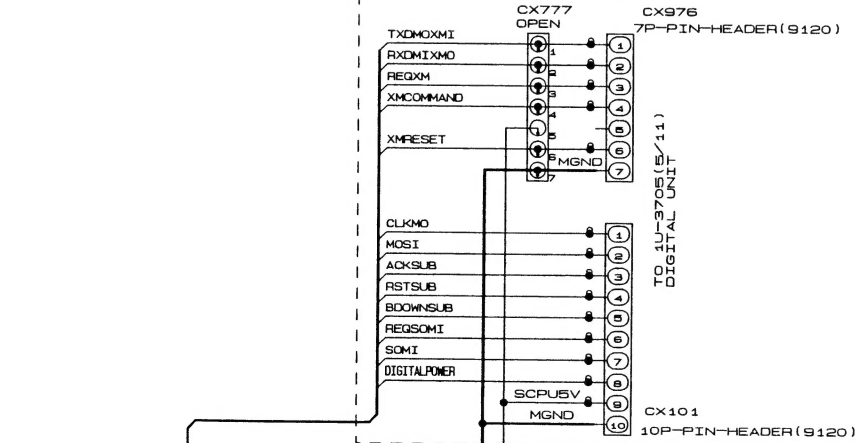
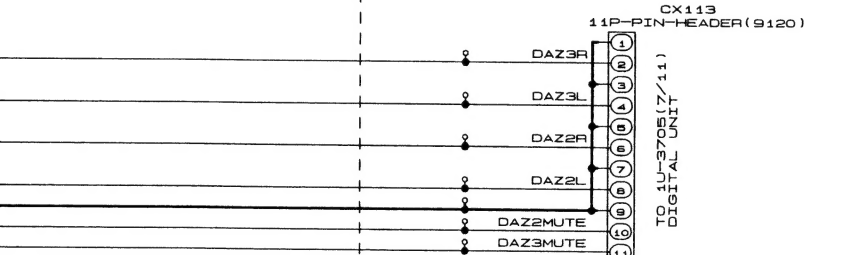
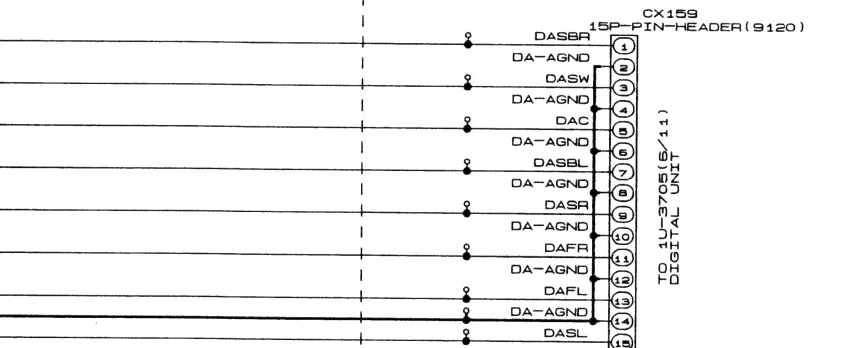
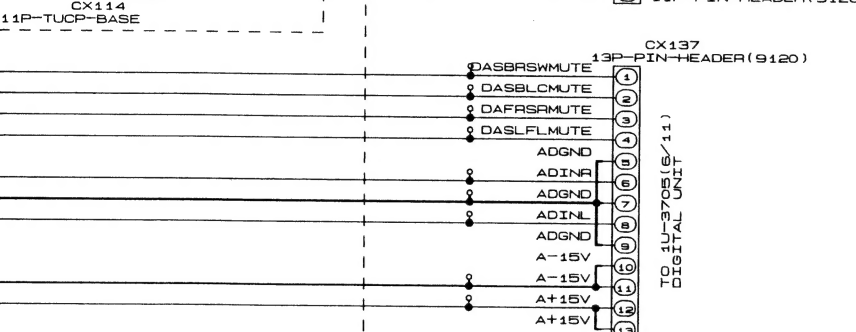
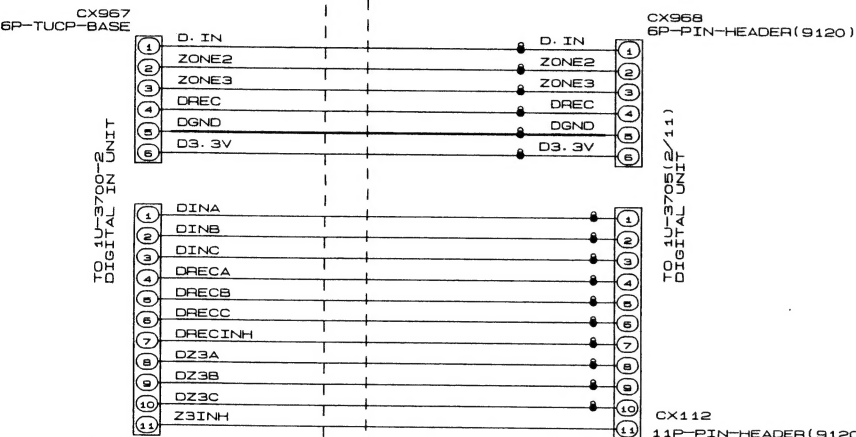


TO D. LINK
1U-3704-2



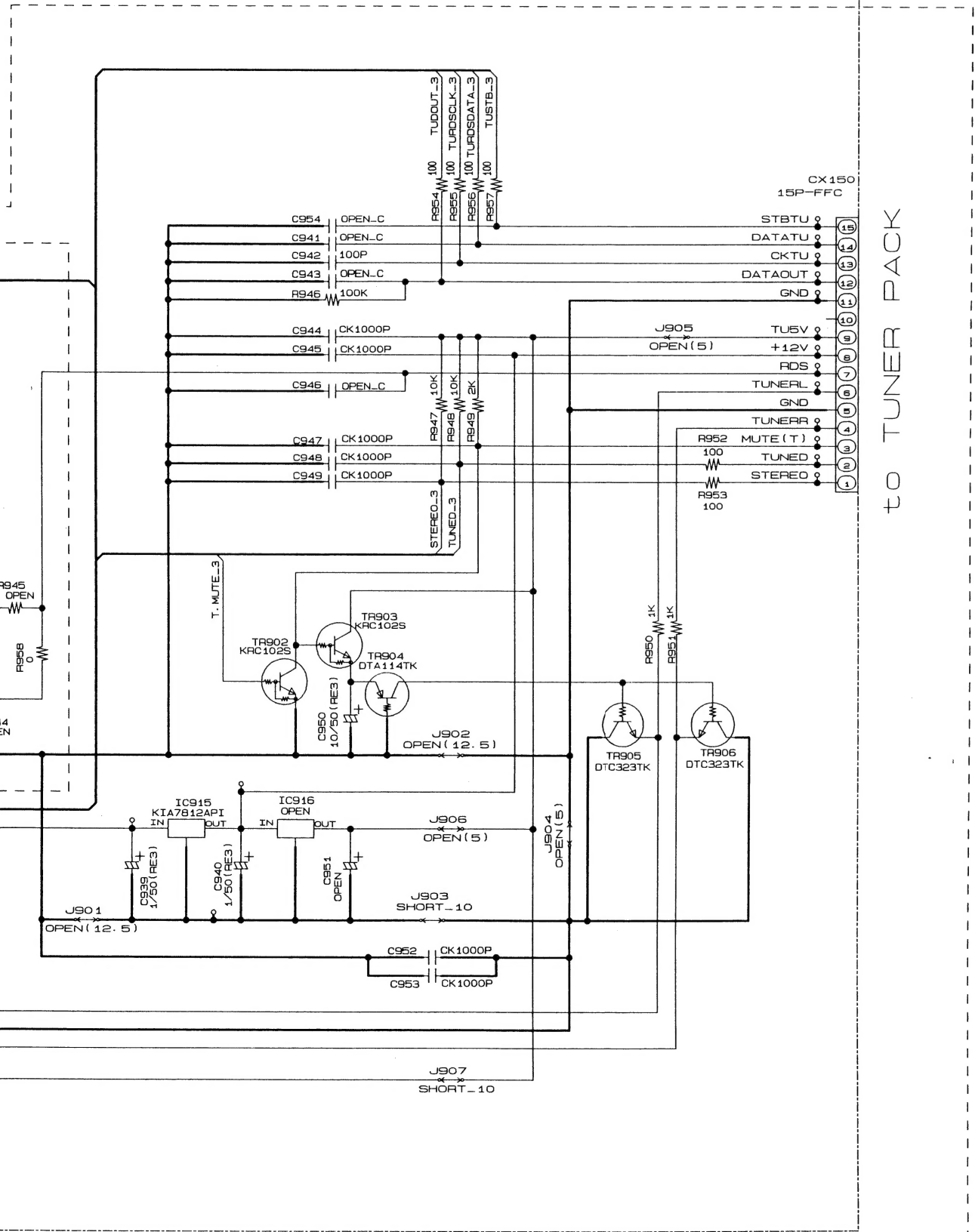
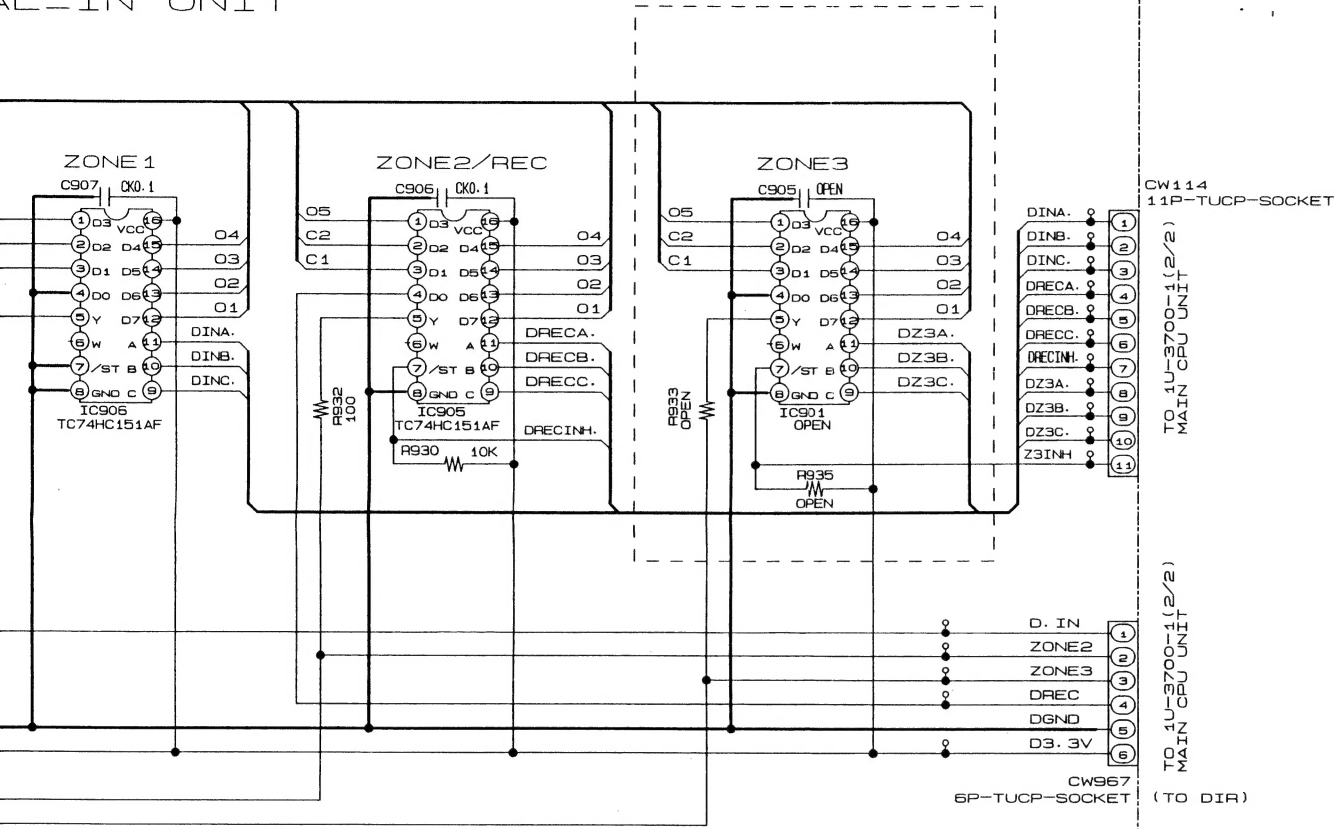
DENON ORIGINAL ERSATZTEILE ORIGINAL SPAREPARTS	
Lagerort / Warehouse Location E014106B	Lieferanten-Nr. / Distributor No. 0250375608
Version / Version 1	
Bestell-Nr. / Order No. 054395300	Datum / Date 28.02.2006
Artikel / Article SCHEMATIC DIAGRAM AVR3806 SMAVR3806	

DENO-00418



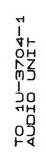


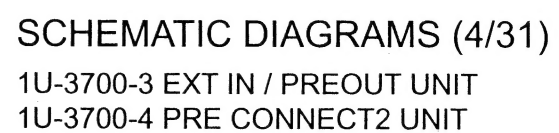
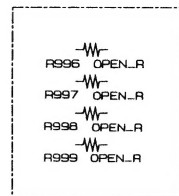
1U-3700-2
DIGITAL IN UNIT



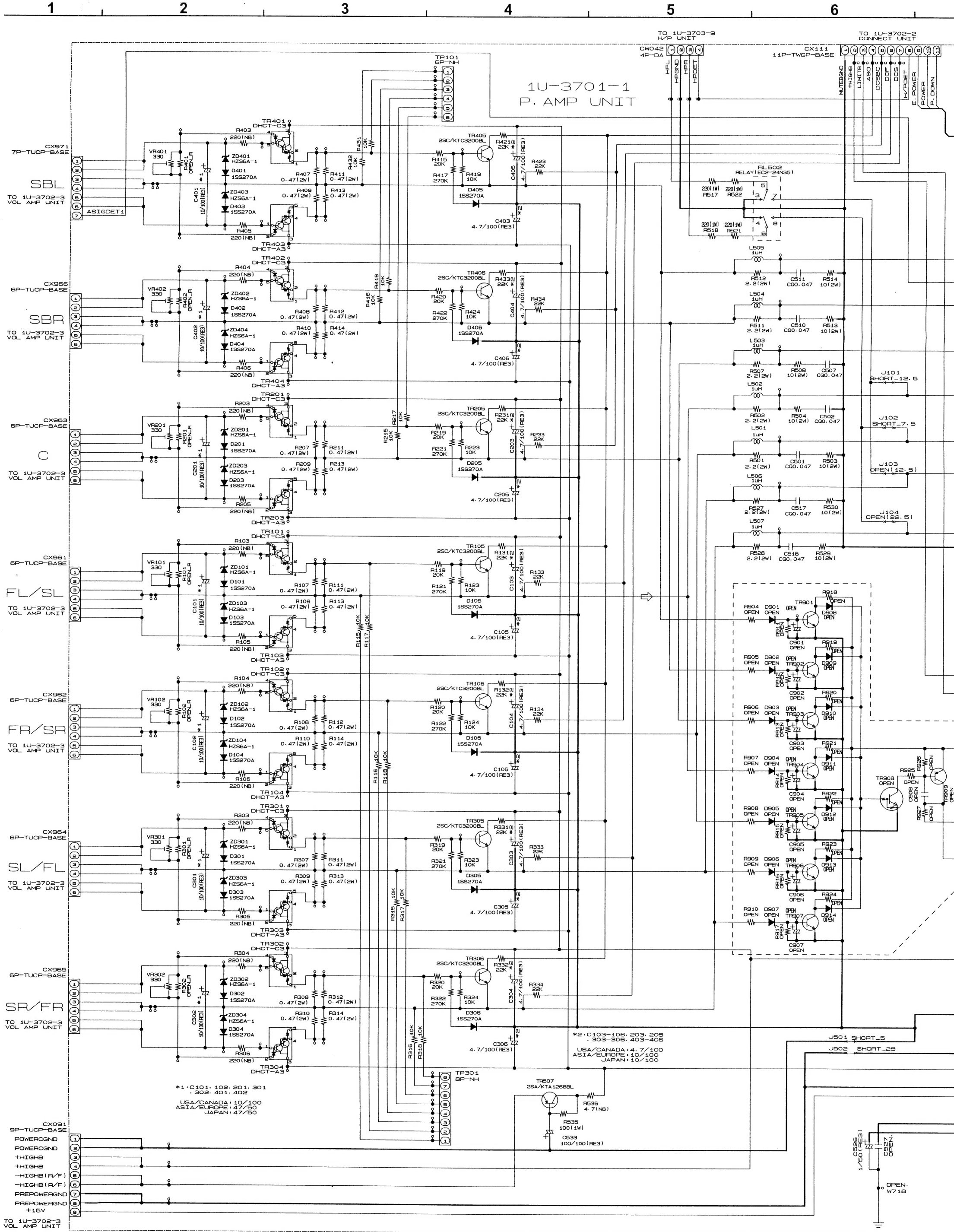
SCHEMATIC DIAGRAMS (3/31)
1U-3700-2 DIGITAL IN UNIT

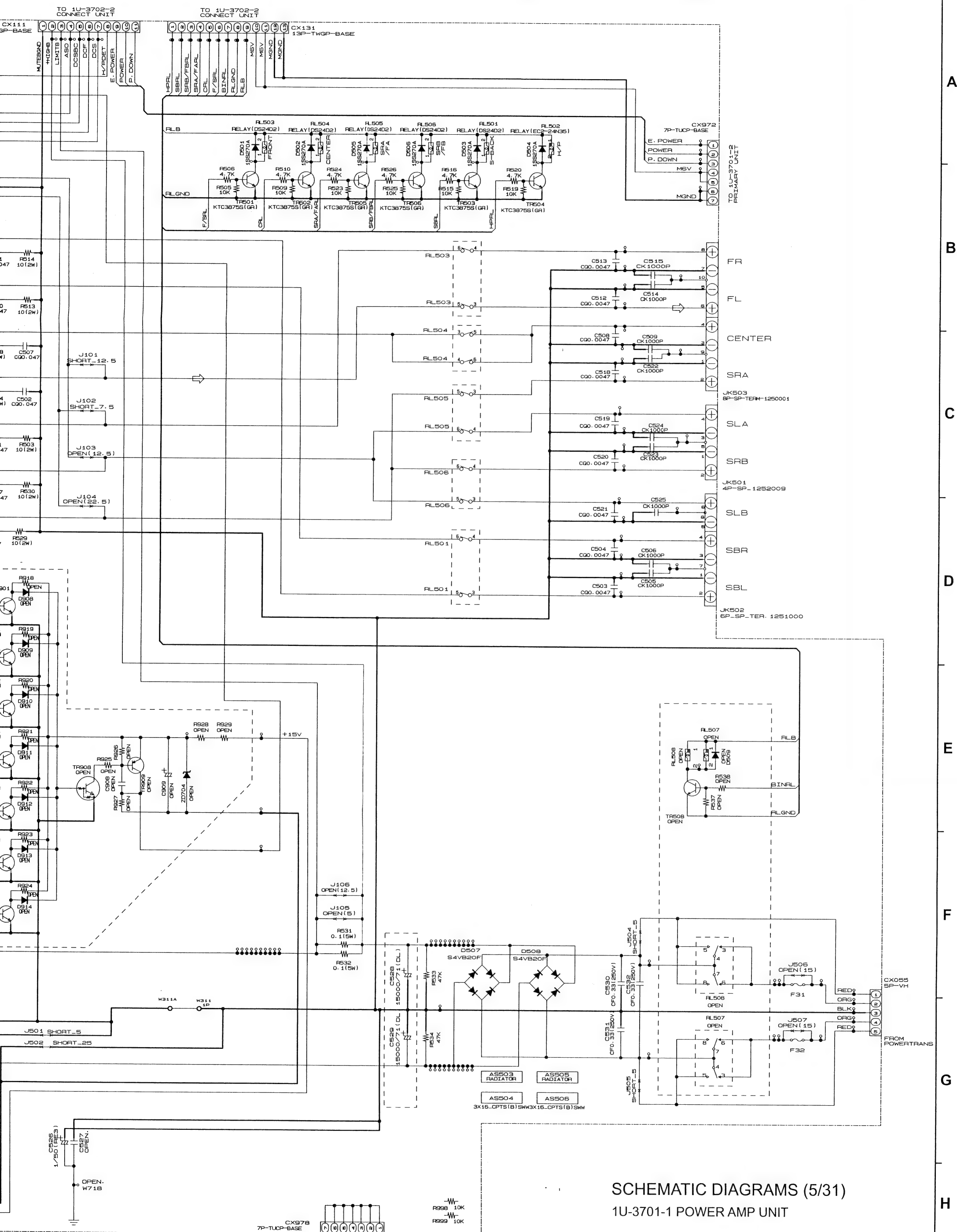
AVR-MODEL ONLY



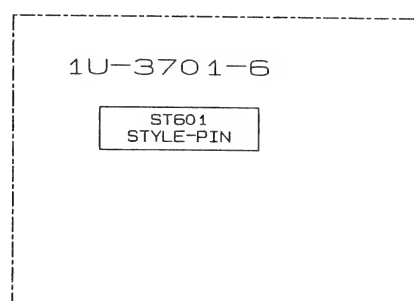


SCHEMATIC DIAGRAMS (5/31)





SCHEMATIC DIAGRAMS (5/31)



AVR3806	
	*1 T602
*USA CANADA	2336524
EUROPE	2336526
ASIA	2336526
CHINA KOREA	2336528
TAIWAN R. O. C	2336524
JAPAN	2330703



	*1 T602	*2 R624	*3 CX025	*4 AC601	*5 F1	*6 F8
*USA CANADA	2336524009	2. 2M	OPEN	AC OUTLET	8A/125V	8A/125V
EUROPE	2336526007	OPEN	2P-VH	OPEN	4A/250V	2. 5A/250V
ASIA	2336526007	OPEN	2P-VH	OPEN	4A/250V	2. 5A/250V
CHINA KOREA	2336528005	OPEN	OPEN	OPEN	4A/250V	OPEN
TAIWAN R. O. C	2336524009	OPEN	OPEN	AC OUTLET	8A/125V	8A/125V
JAPAN	2330703004	OPEN	OPEN	AC OUTLET	10A/125V	8A/125V

SCHEMATIC DIAGRAMS (7/31)

1

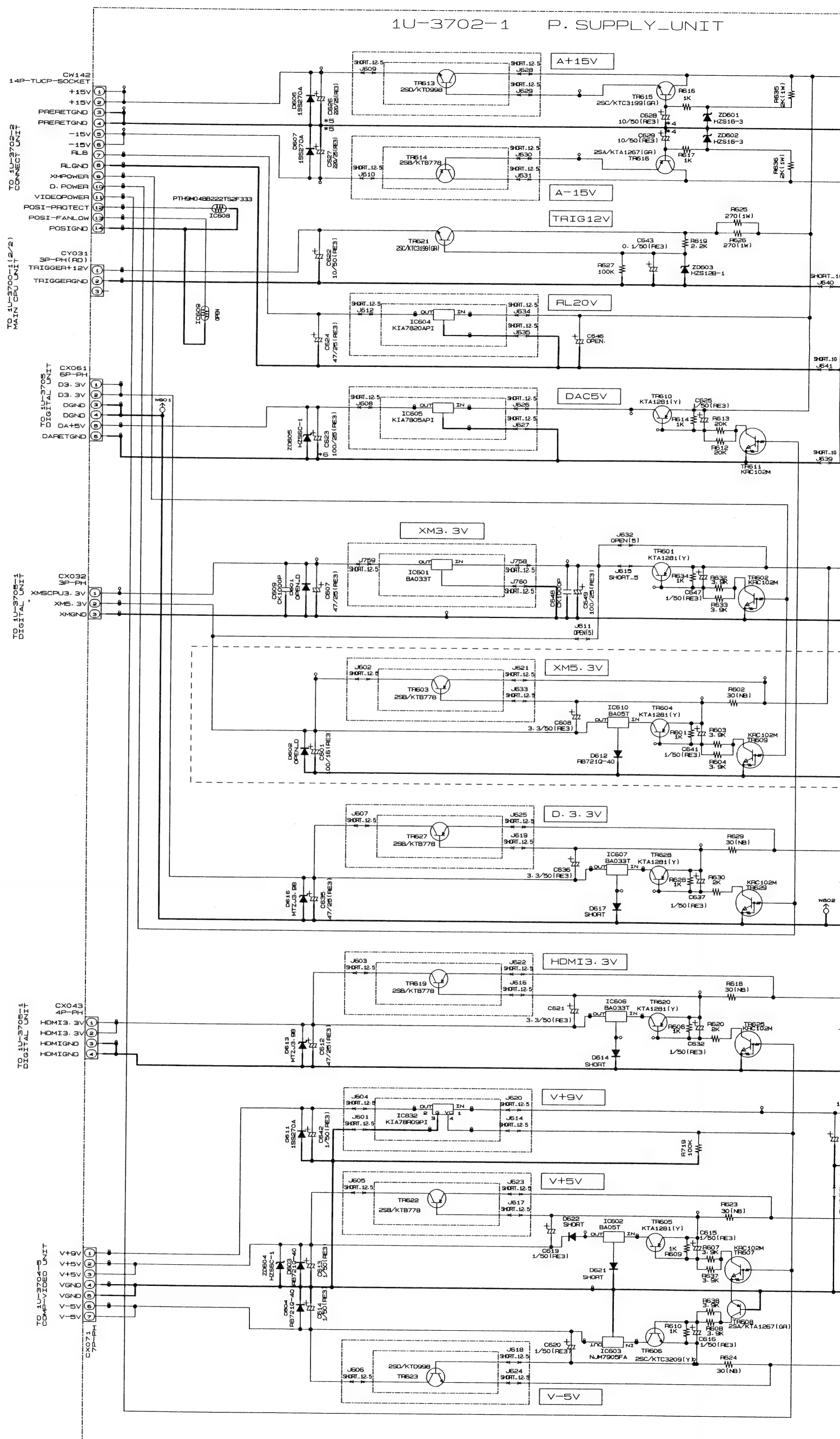
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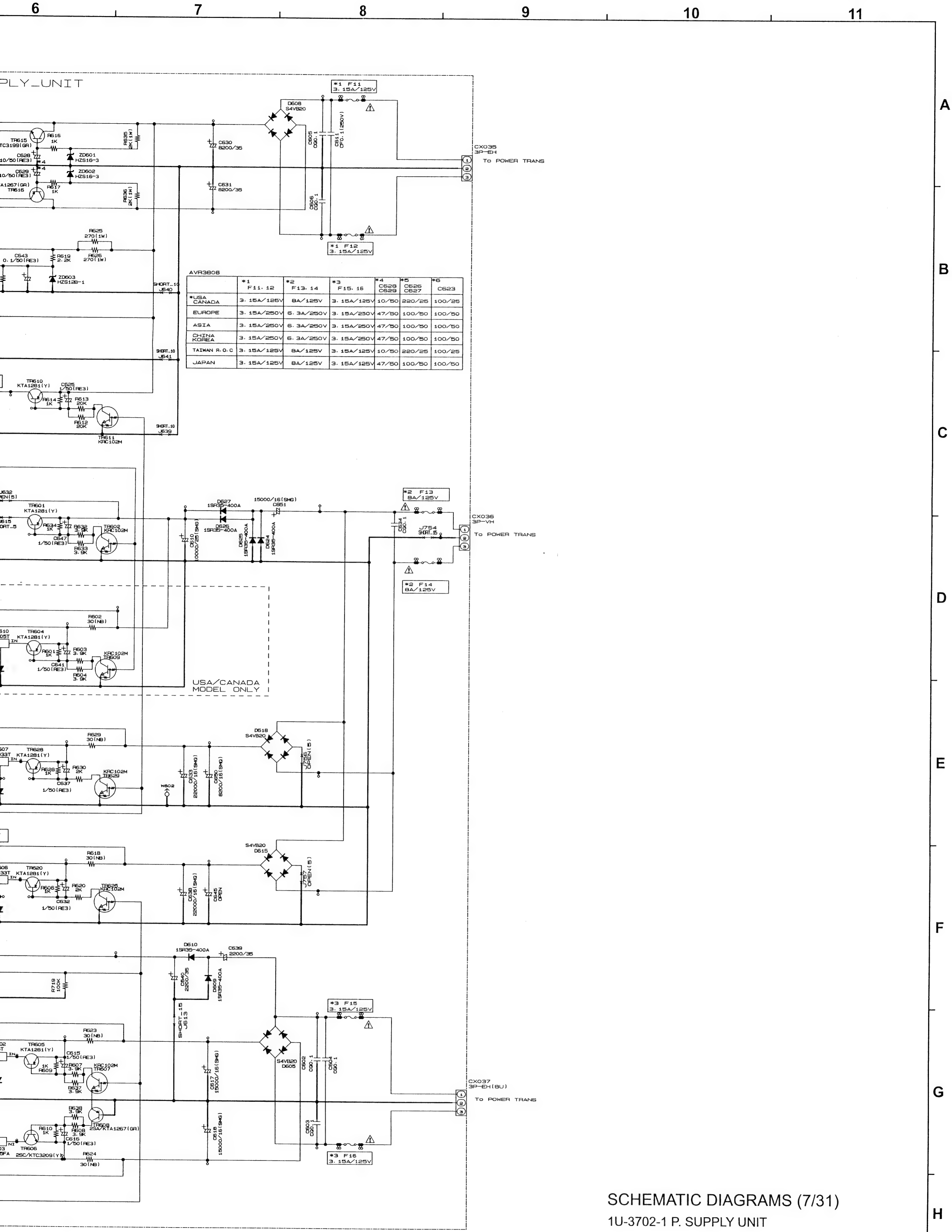
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4

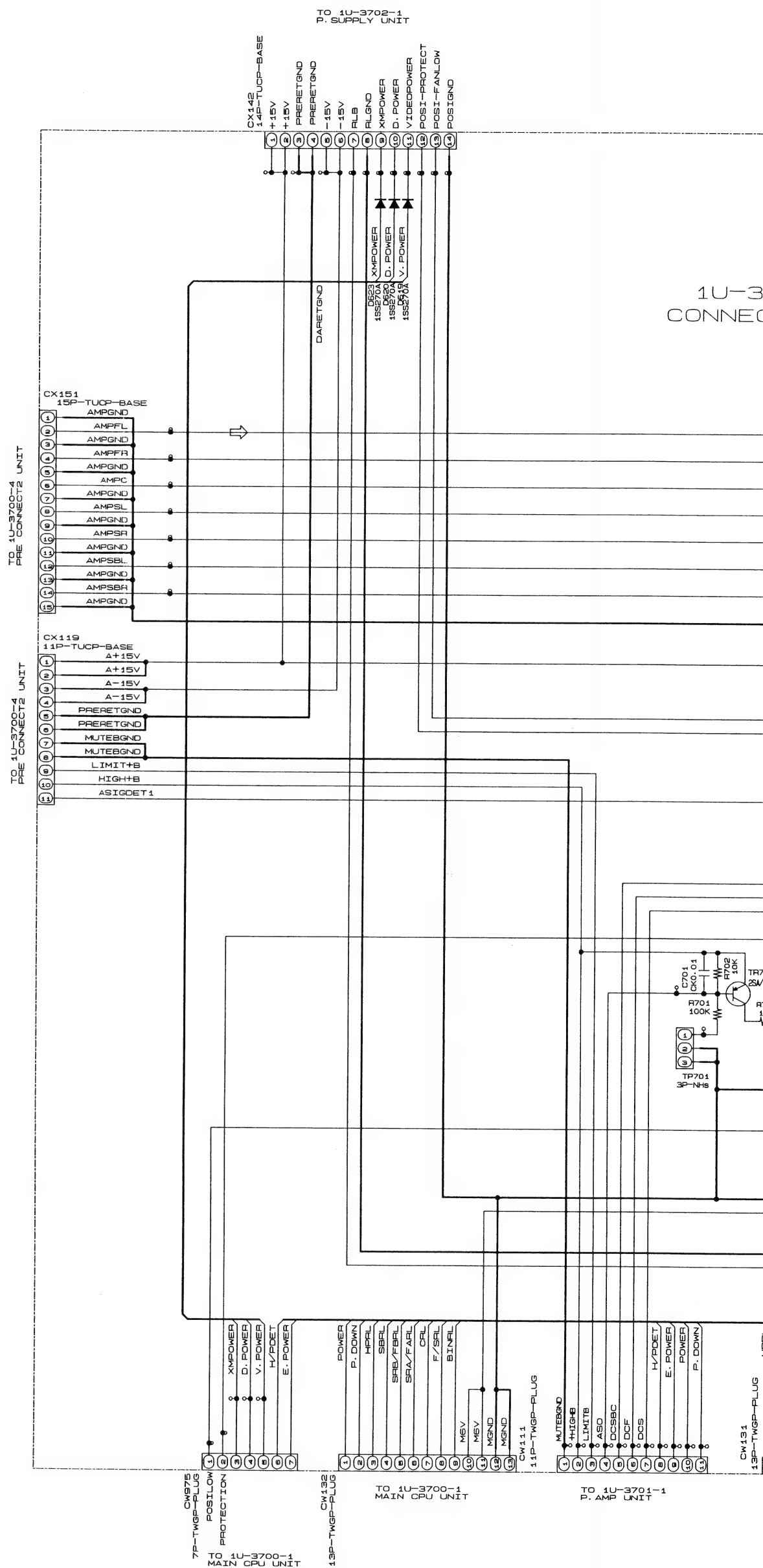
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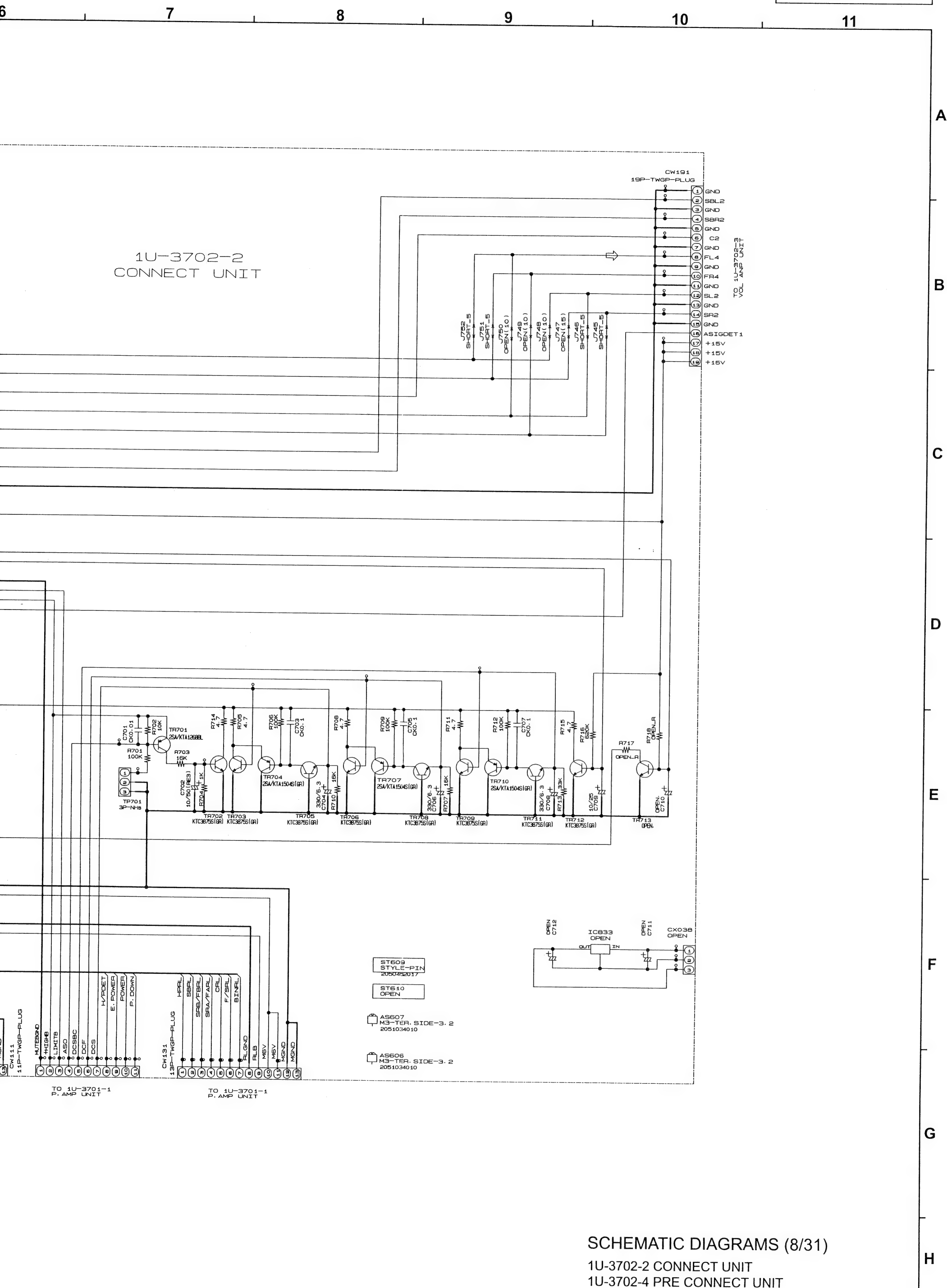
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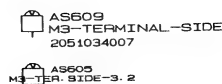




SCHEMATIC DIAGRAMS (7/31)
1U-3702-1 P. SUPPLY UNIT







6

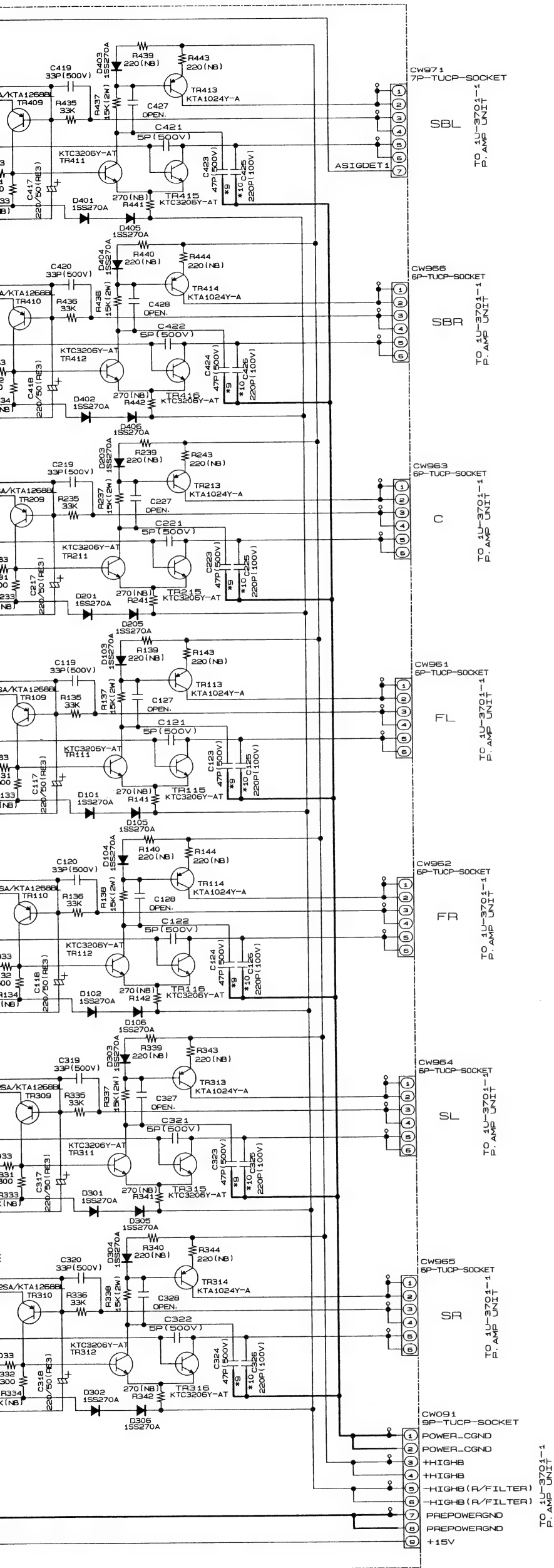
7

8

9

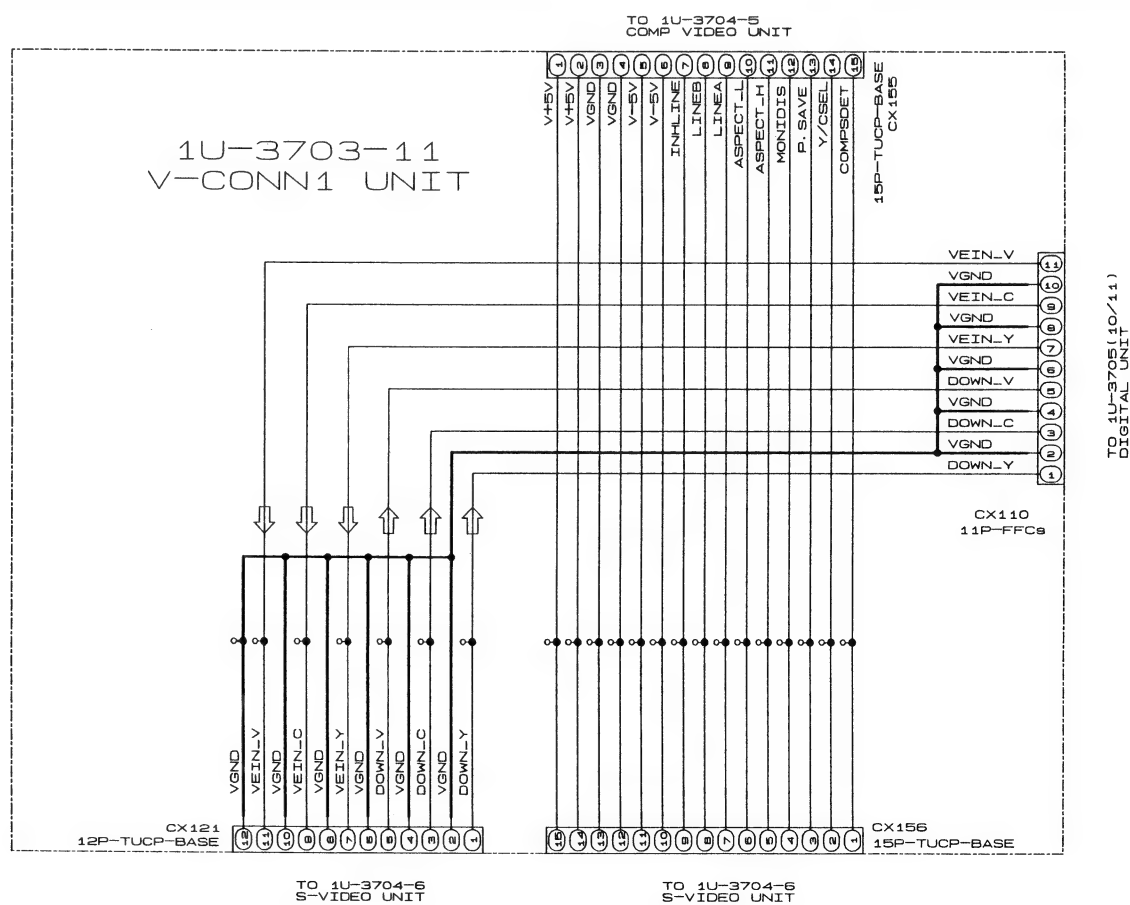
10

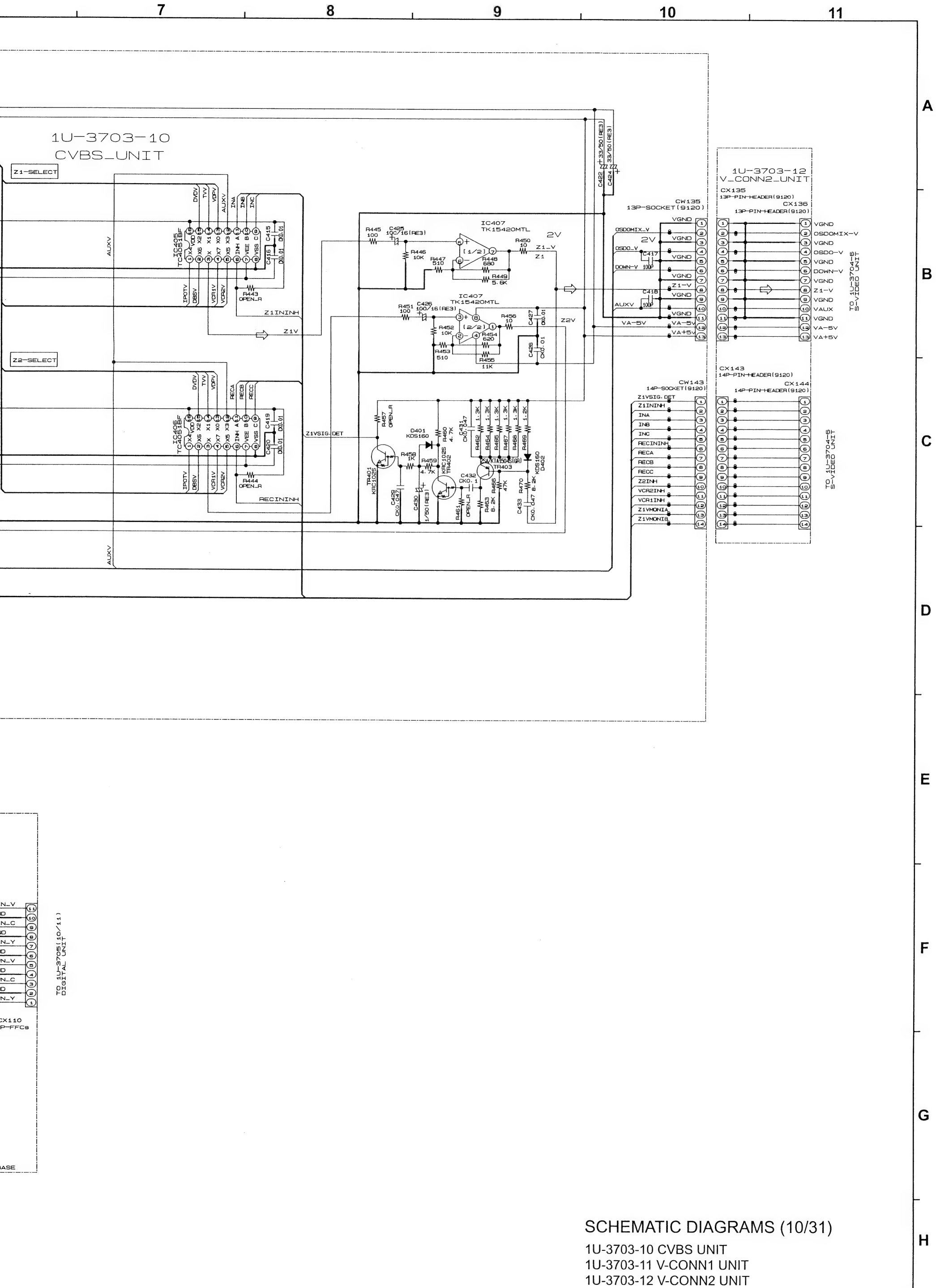
11

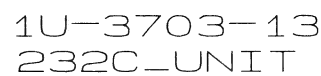


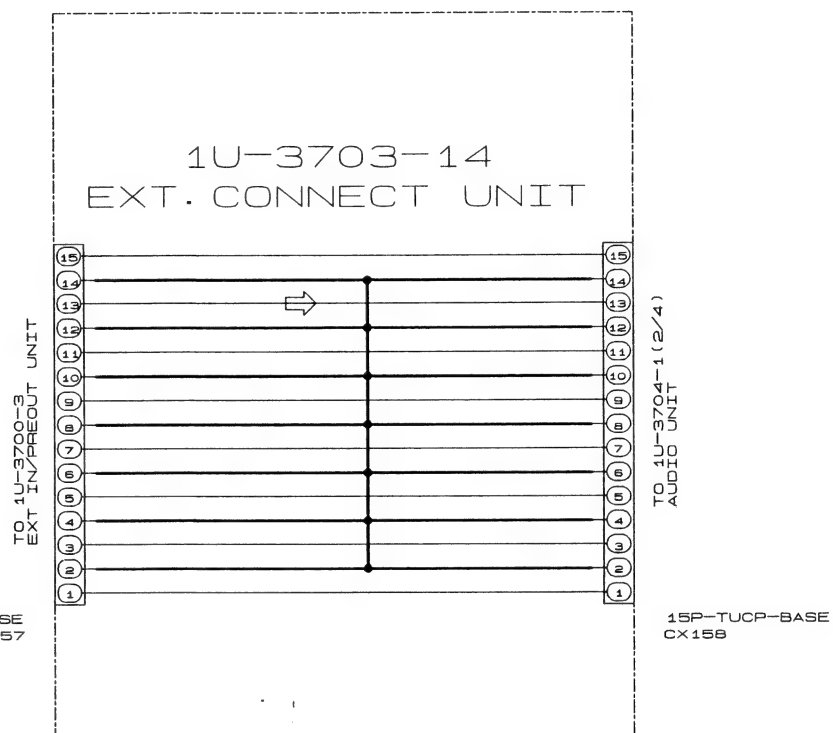
SCHEMATIC DIAGRAMS (9/31)
1U-3702-3 VOLAMP UNIT

A horizontal number line with six segments labeled 1 through 6. Segment 1 is shaded light blue, segment 2 is light green, segment 3 is light red, segment 4 is light orange, segment 5 is light purple, and segment 6 is light yellow.



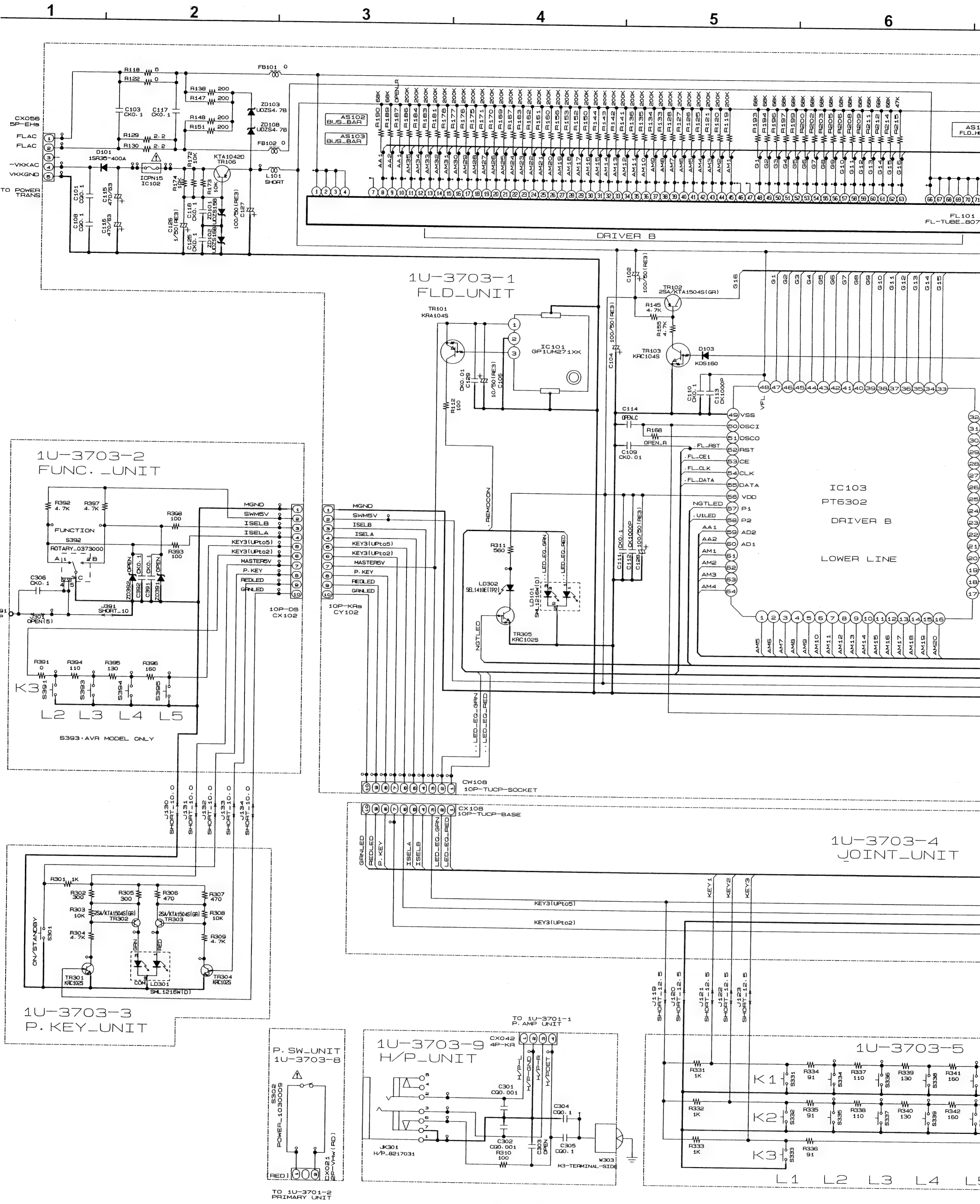






SCHEMATIC DIAGRAMS (11/31)
1U-3703-13 232C UNIT
1U-3703-14 EXT. CONNECT UNIT

SCHEMATIC DIAGRAMS (12/31)



	L1	L2	L3	L4	L5	L6	L7	L8	L9	L10
K1	PURE DIRECT	STAN DARD	5CH7CH STEREO	DSP SIMU.	LEFT	UP	SUPP. PARA.	TONE CONTORL	TONE DEFEAT	STATUS
K2	DIRECT	MODE	ANALOG	EXT. IN	SYSTEM SETUP	DOWN	ENTER	RIGHT	SUPP. BACK	EQ ON/OFF
K3	STEREO	SOURCE	TUNING PRESET	ZONE2/ RECSL	VIDEO SELECT			USER1	USER2	USER3

SOURCE

TUNING PRESET

ZONE2/ RECSL

VIDEO SELECT

PURE DIRECT

DIRECT

STEREO

STAN DARD

5CH7CH STEREO

DSP SIMU.

UP

SUPP. PARA.

TONE CONTORL

TONE DEFEAT

STATUS

EQ ON/OFF

USER1

LEFT

ENTER

RIGHT

MODE

ANALOG

EXT. IN

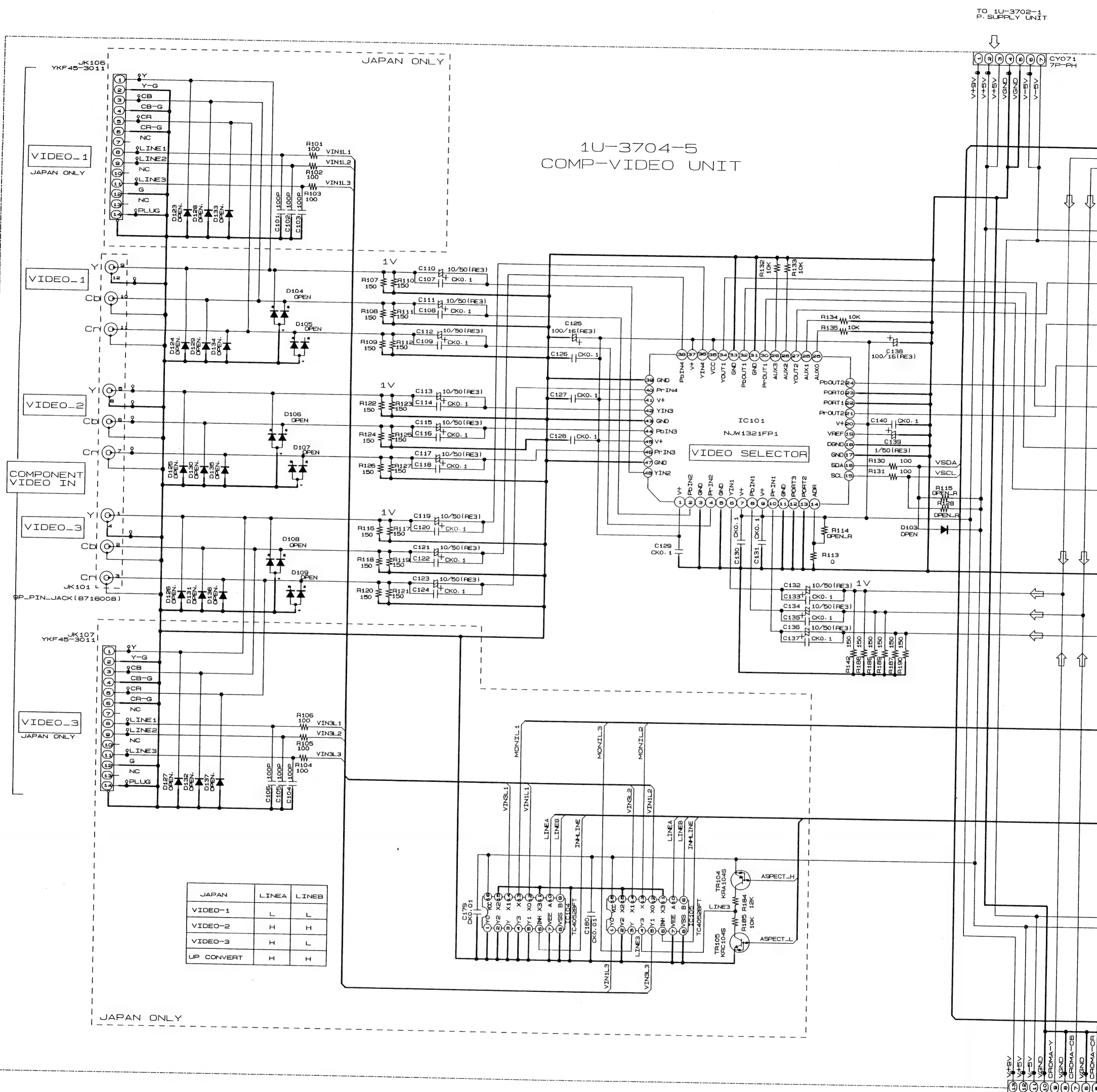
SYSTEM SETUP

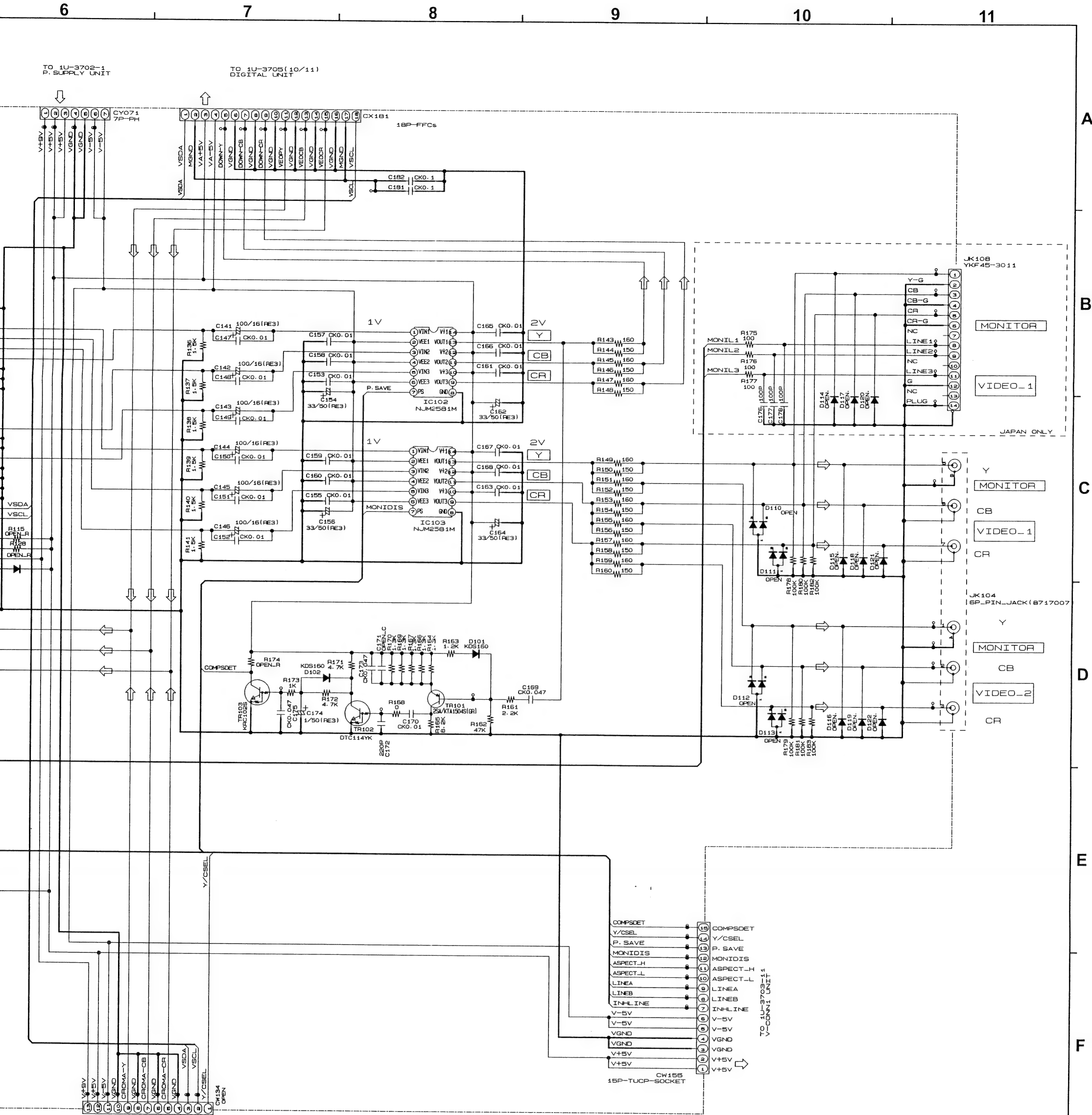
DOWN

SUPP. BACK

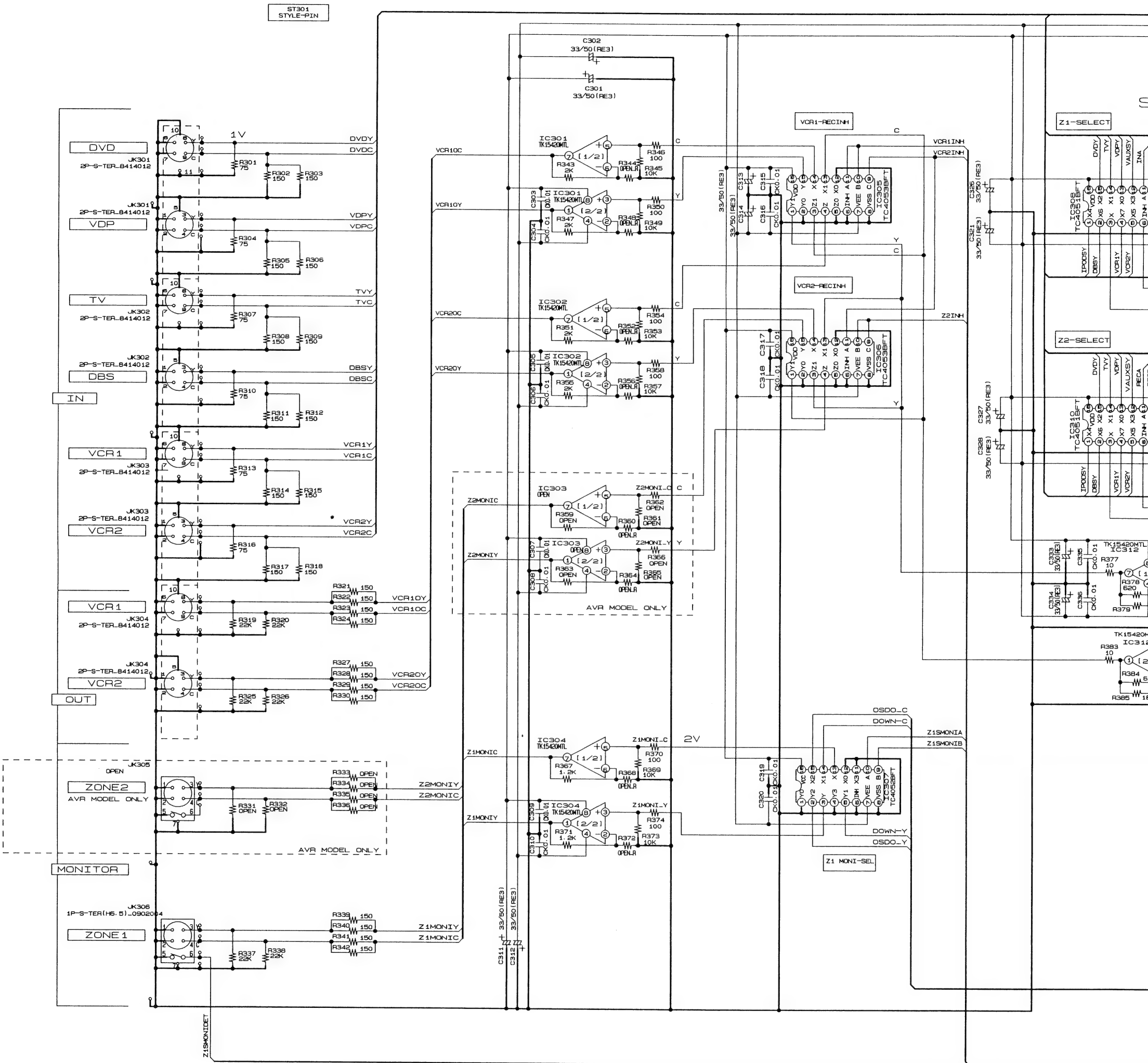


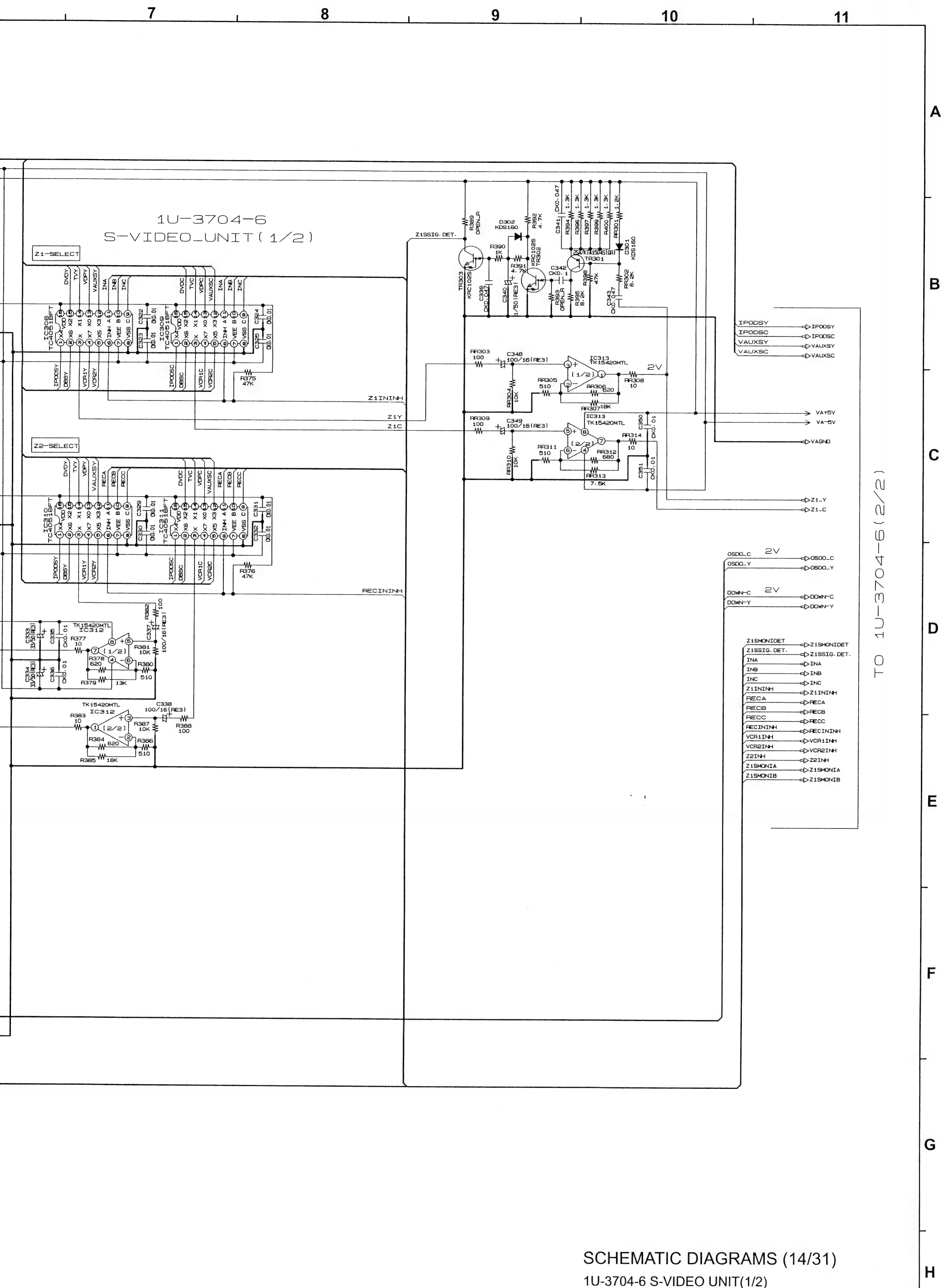
1U-3703-1 FLD UNIT
1U-3703-2 FUNC. UNIT
1U-3703-3 P.KEY UNIT
1U-3703-4 JOINT UNIT
1U-3703-5 TACT-1 UNIT
1U-3703-6 TACT-2 UNIT
1U-3703-7 VOL UNIT
1U-3703-8 P.SW UNIT
1U-3703-9 H/P UNIT



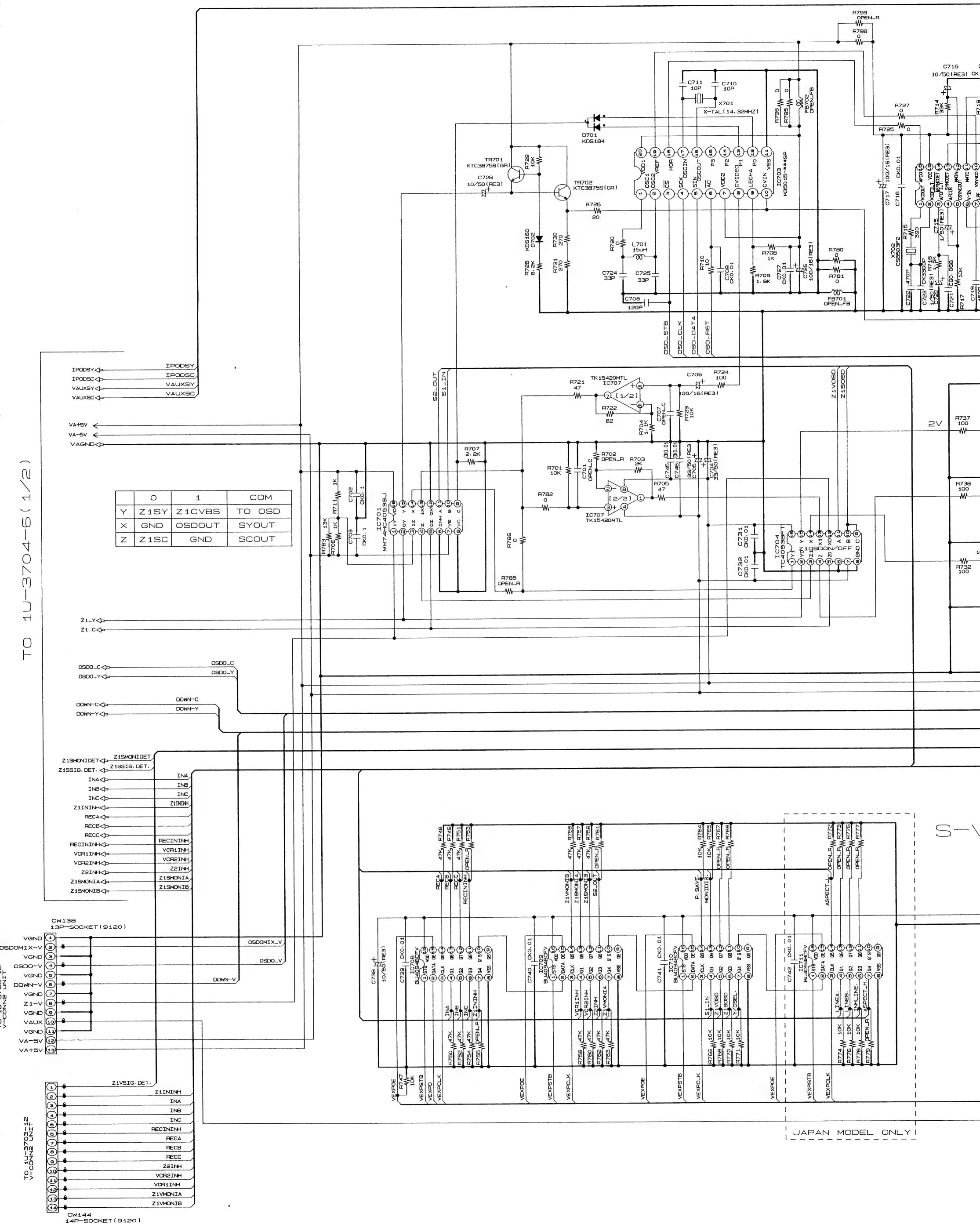


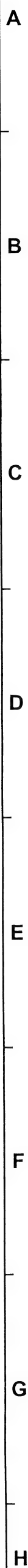
COMPONENT
VIDEO





TO 1U-3703-12
Y-COMMS UNIT

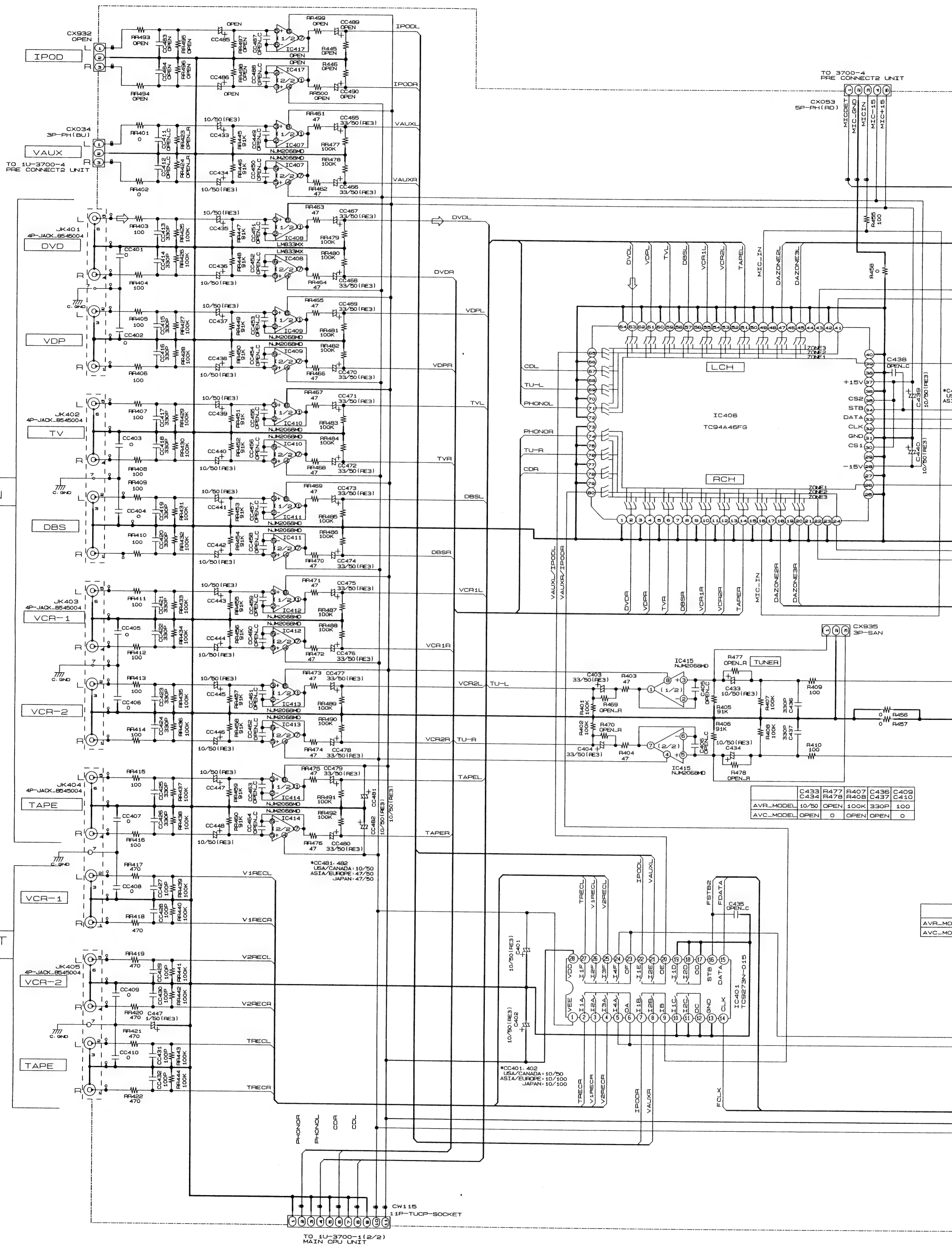




TOP

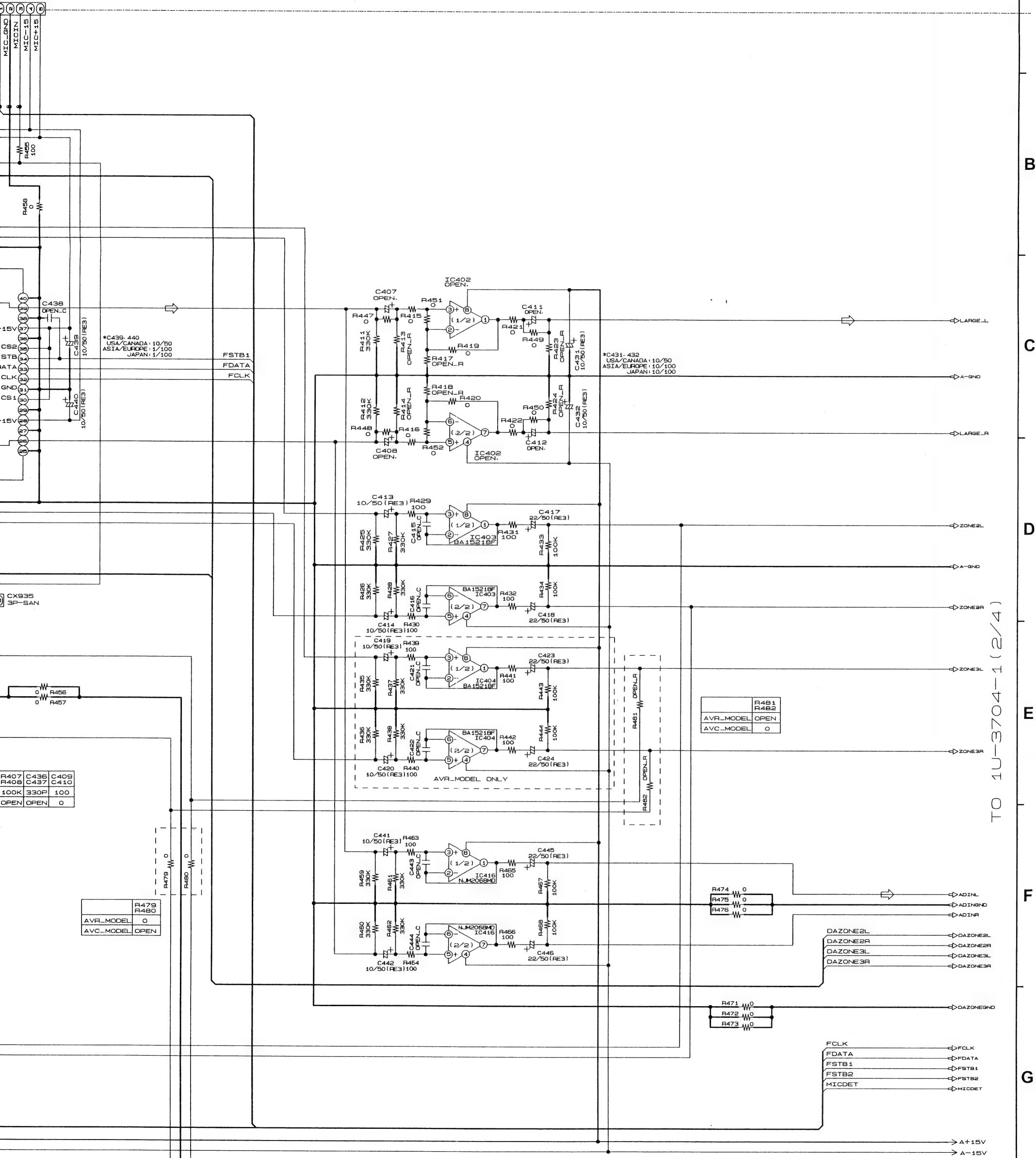
TO 1U-3700-1(2/2)
MAIN CPU UNIT

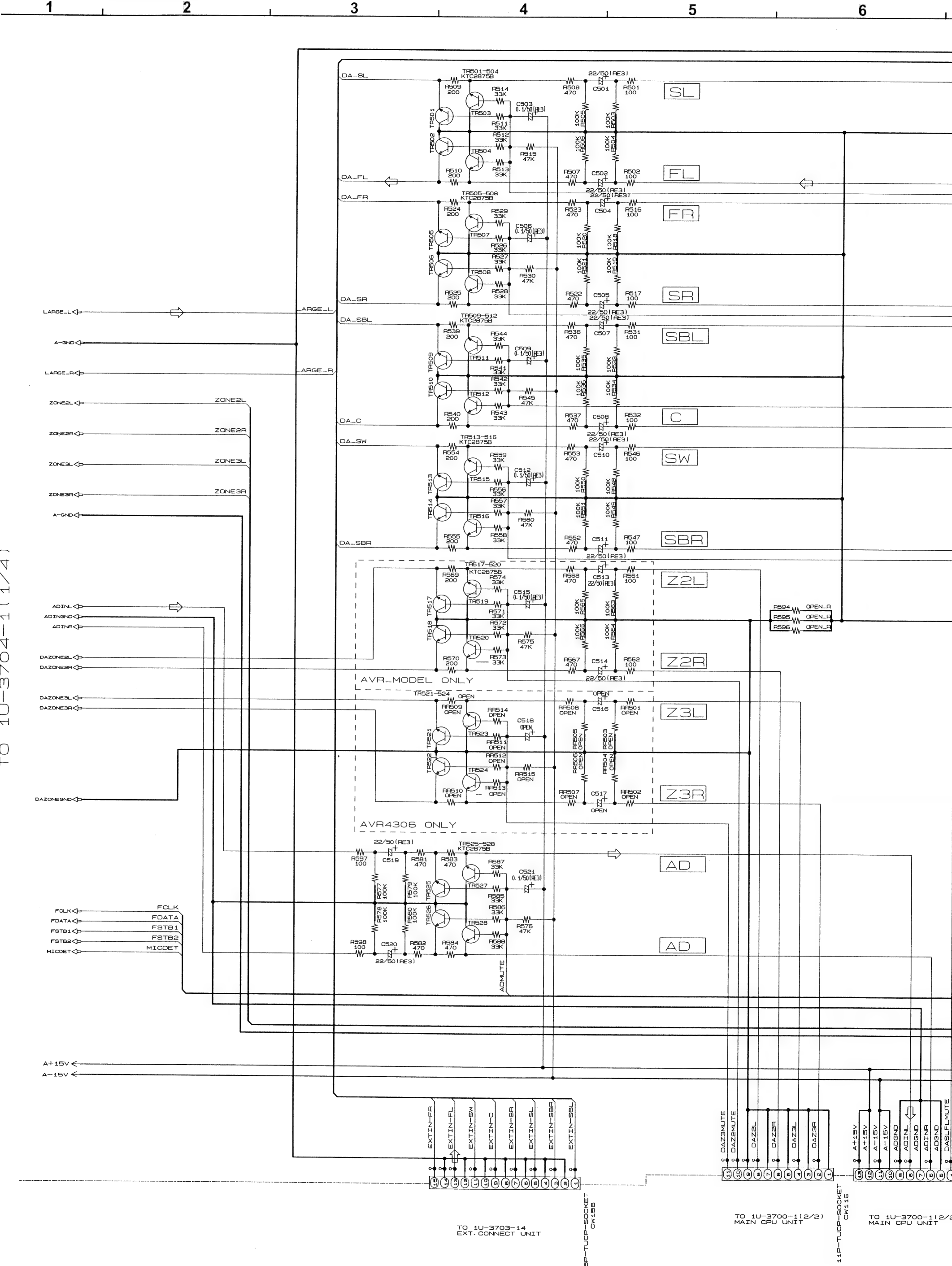
JAPAN MODEL ONLY

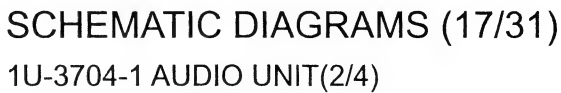


1U-3704-1 (1 / 4)
AUDIO_UNIT (1 / 4)

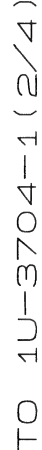
NO-4
CONNECT2 UNIT

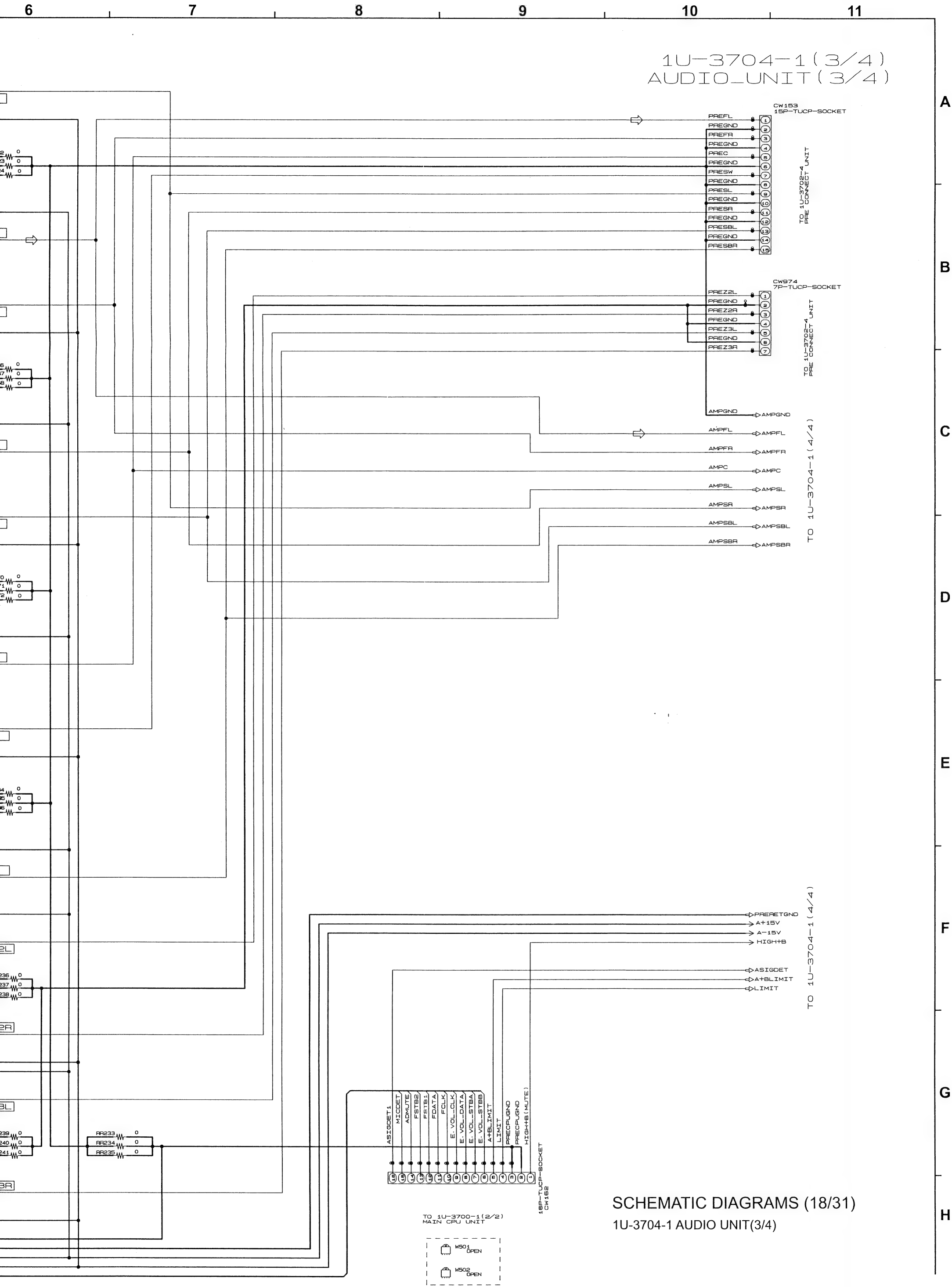






A horizontal number line with tick marks labeled 1, 2, 3, 4, 5, and 6.

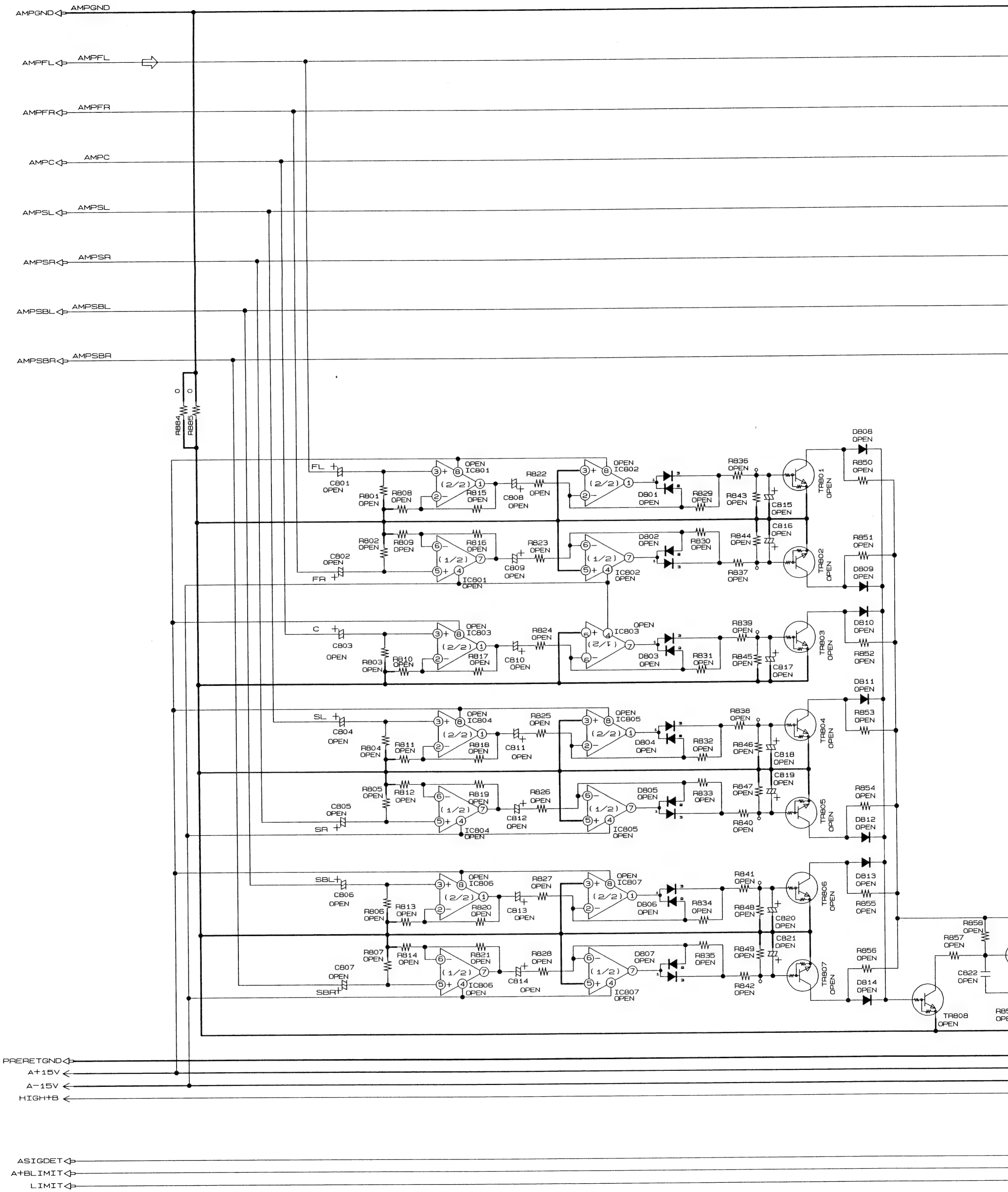




SCHEMATIC DIAGRAMS (18/31)
1U-3704-1 AUDIO UNIT(3/4)

TO 1U-3704-1(3/4)

TO 1U-3704-1(3/4)



6

7

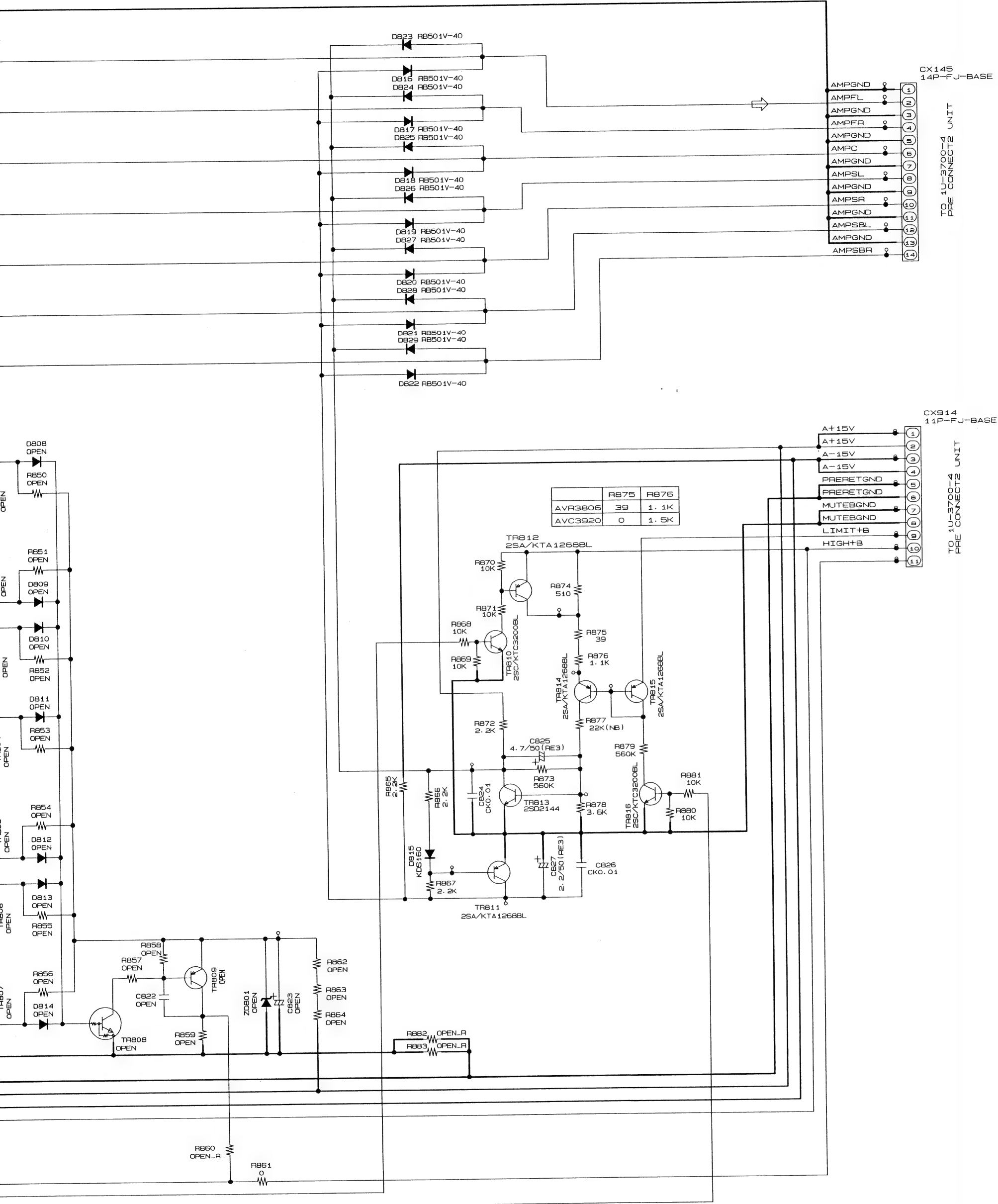
8

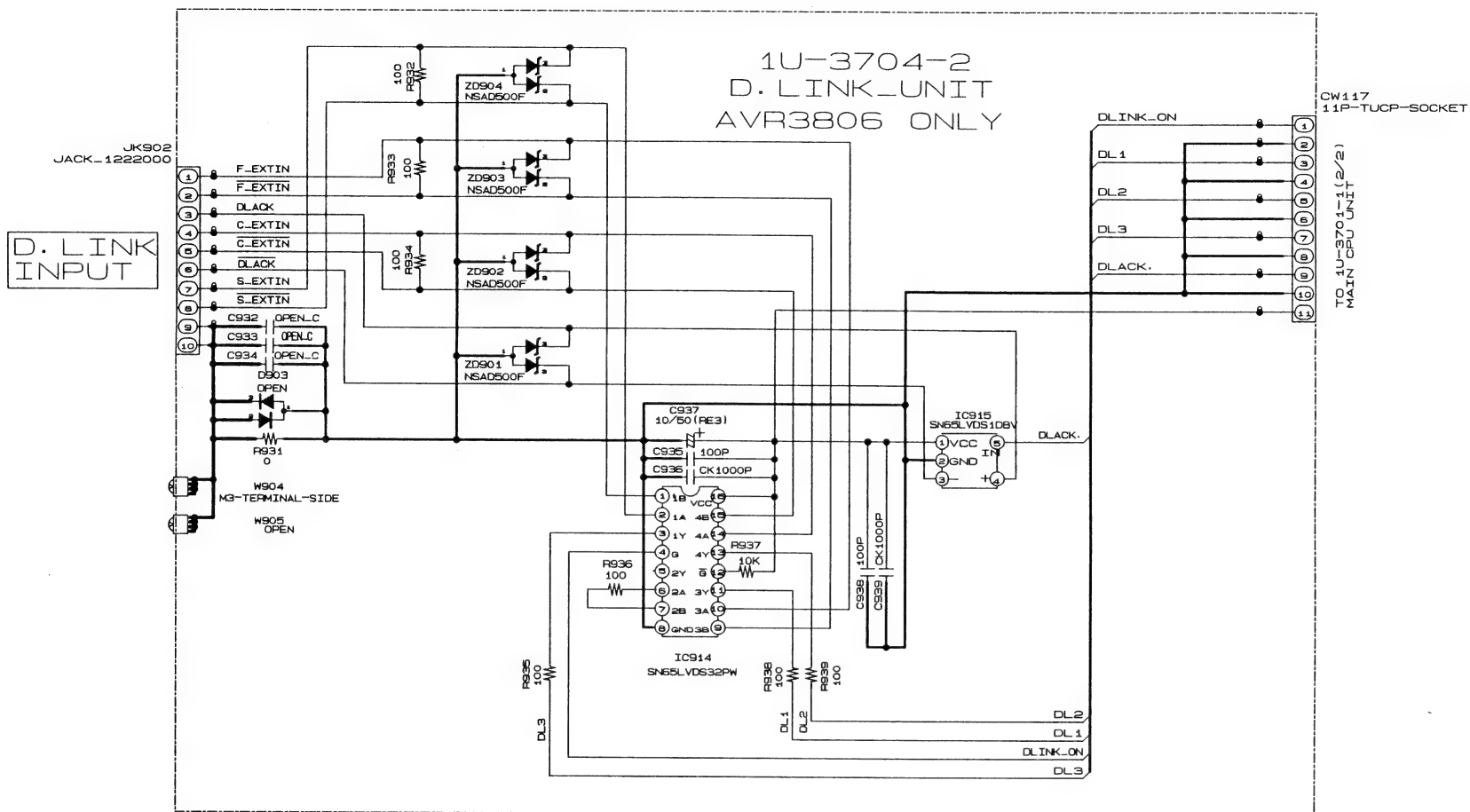
9

10

11

1U-3704-1 (4/4)
AUDIO UNIT (4/4)

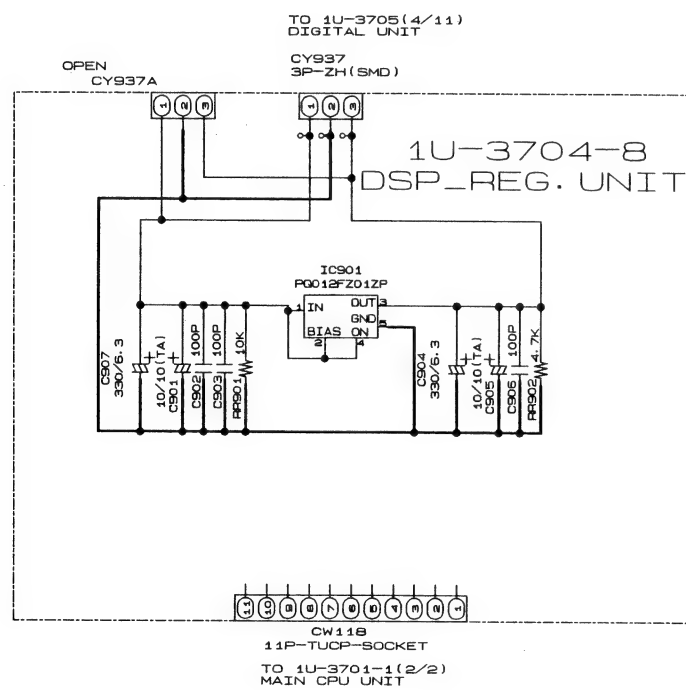




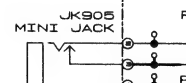
FRONT
V. AUX

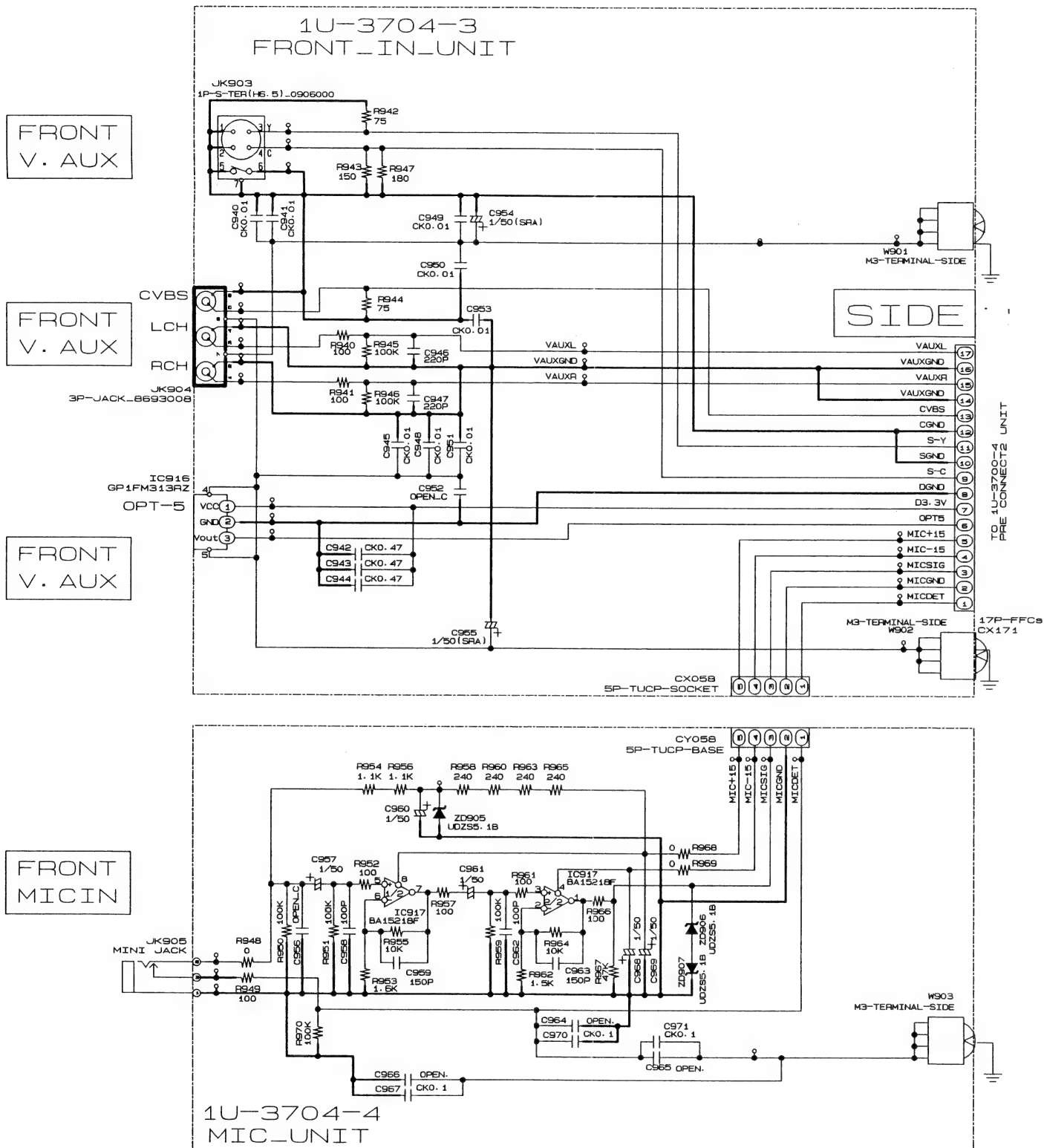
FRONT
V. AUX

FRONT
V. AUX



FRONT
MICIN

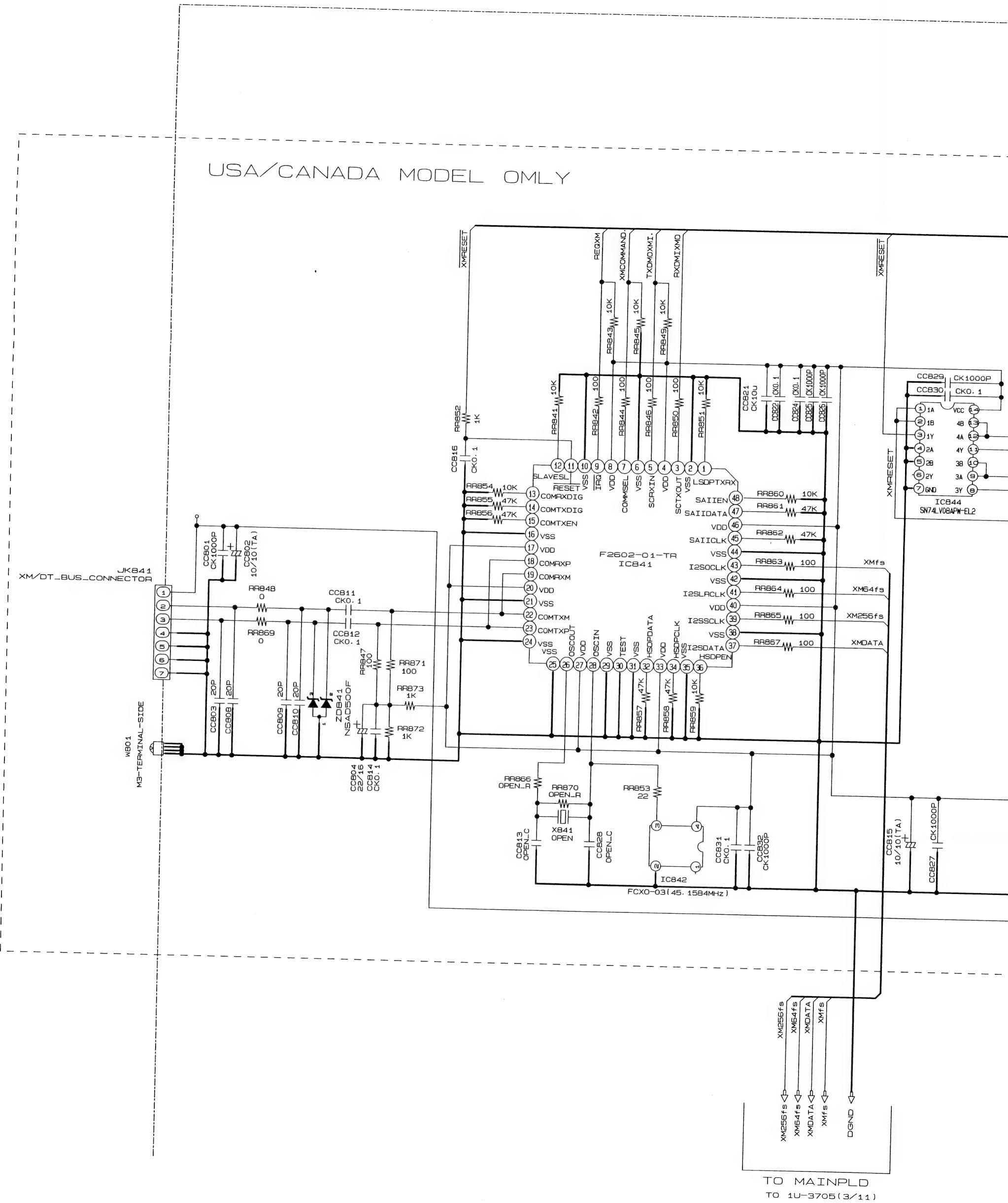




SCHEMATIC DIAGRAMS (20/31)

1U-3704-2 D.LINK UNIT
 1U-3704-3 FRONT IN UNIT
 1U-3704-4 MIC UNIT
 1U-3704-8 DSP REG. UNIT

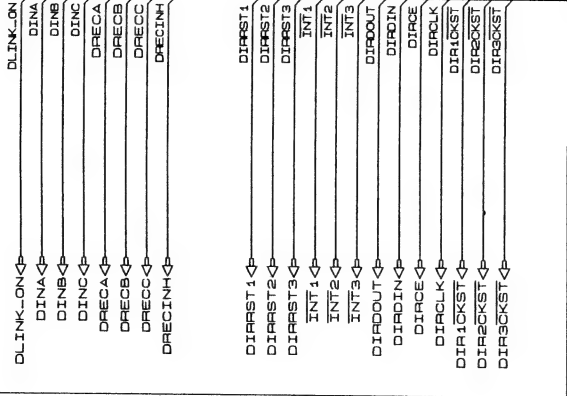
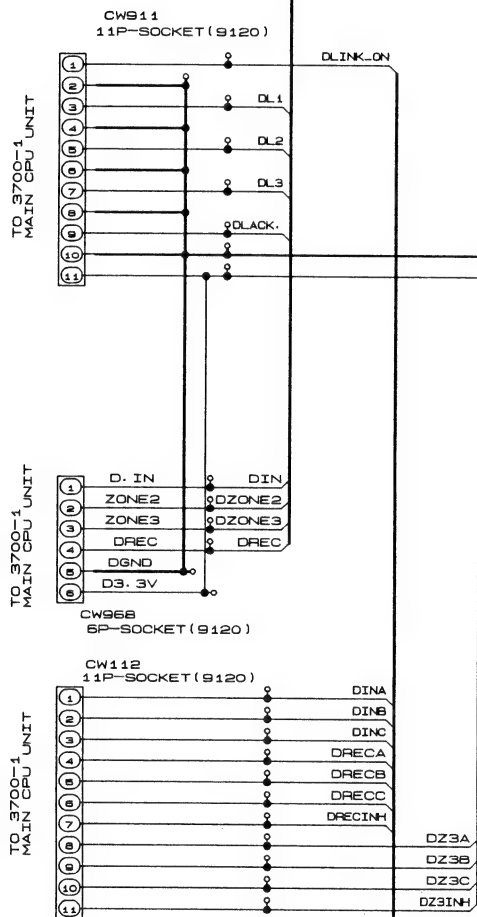
USA/CANADA MODEL ONLY



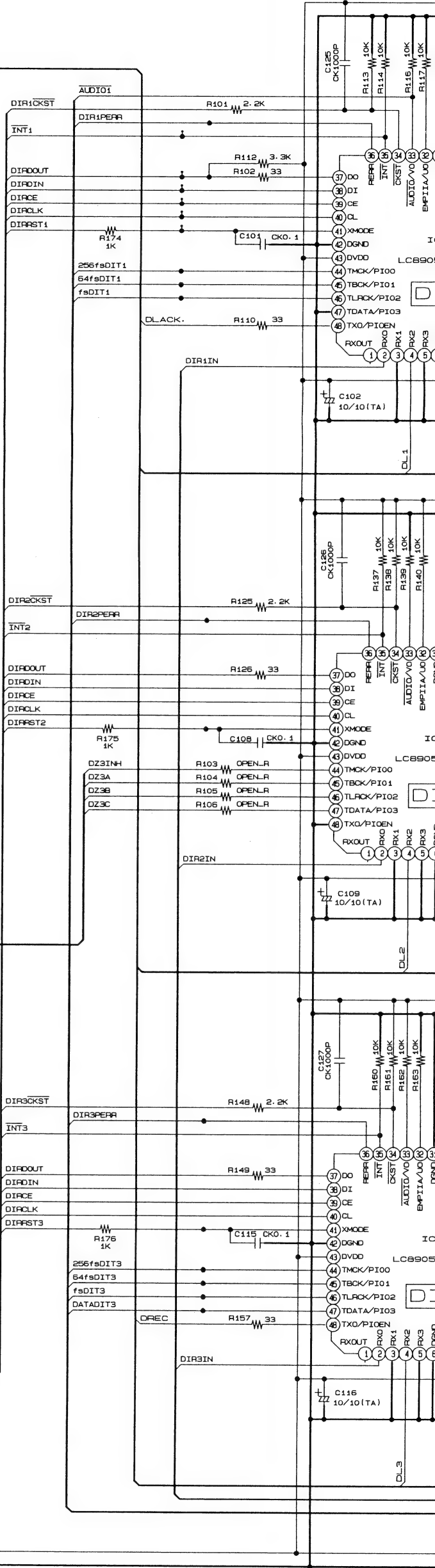
The schematic diagram illustrates the internal wiring of the P. SUPPLY UNIT. It features two main integrated circuits: the ICB44 SN74LV08APW-EL2 (a hex inverters chip) and the CY032 3P-ZH(SMD) connector. The ICB44 is connected to various power and signal lines. Power supply connections include XM5.3V, XM3V, and XMGND. Signal lines include TXDMOXMI, XMCOMMAND, REQXM, and RXDMIXMO. The diagram also shows several capacitors: CC829, CC830, CC815, CC827, CC818, CC834, and CC835. The connections are detailed with pin numbers and component values.

SCHEMATIC DIAGRAMS (21/31)
1U-3705 DIGITAL UNIT(1/11)

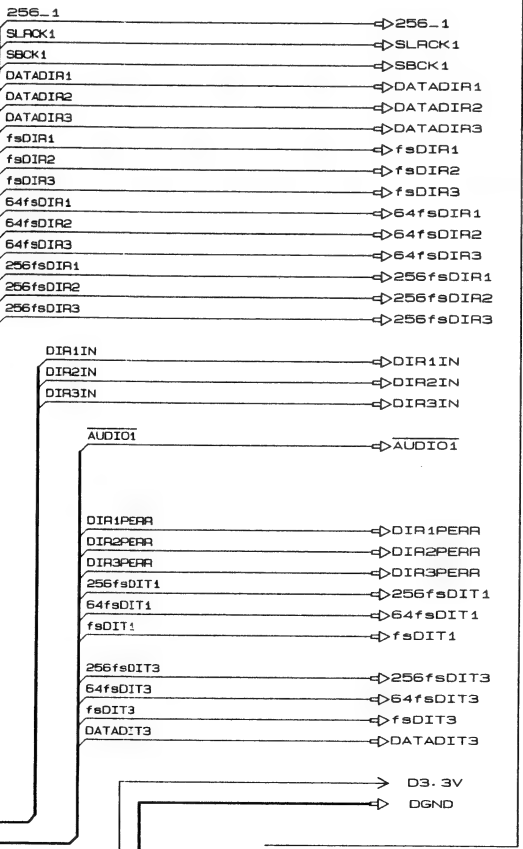
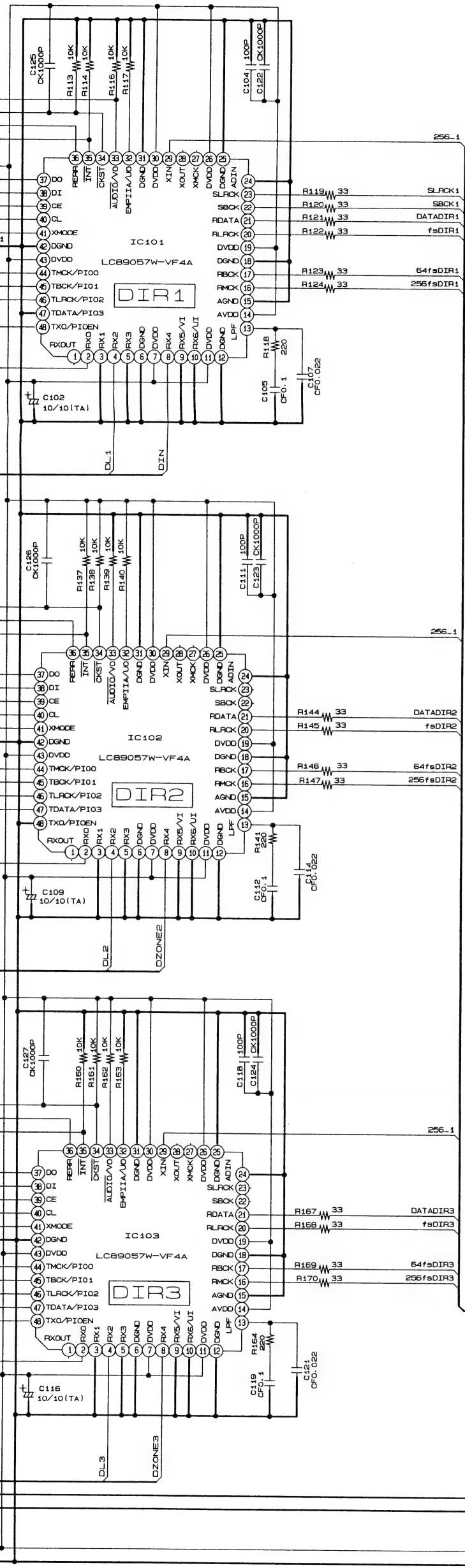
1U-3705
DIGITAL UNIT
(2/11)



TO SUBCPU
TO 1U-3705 (5/11)

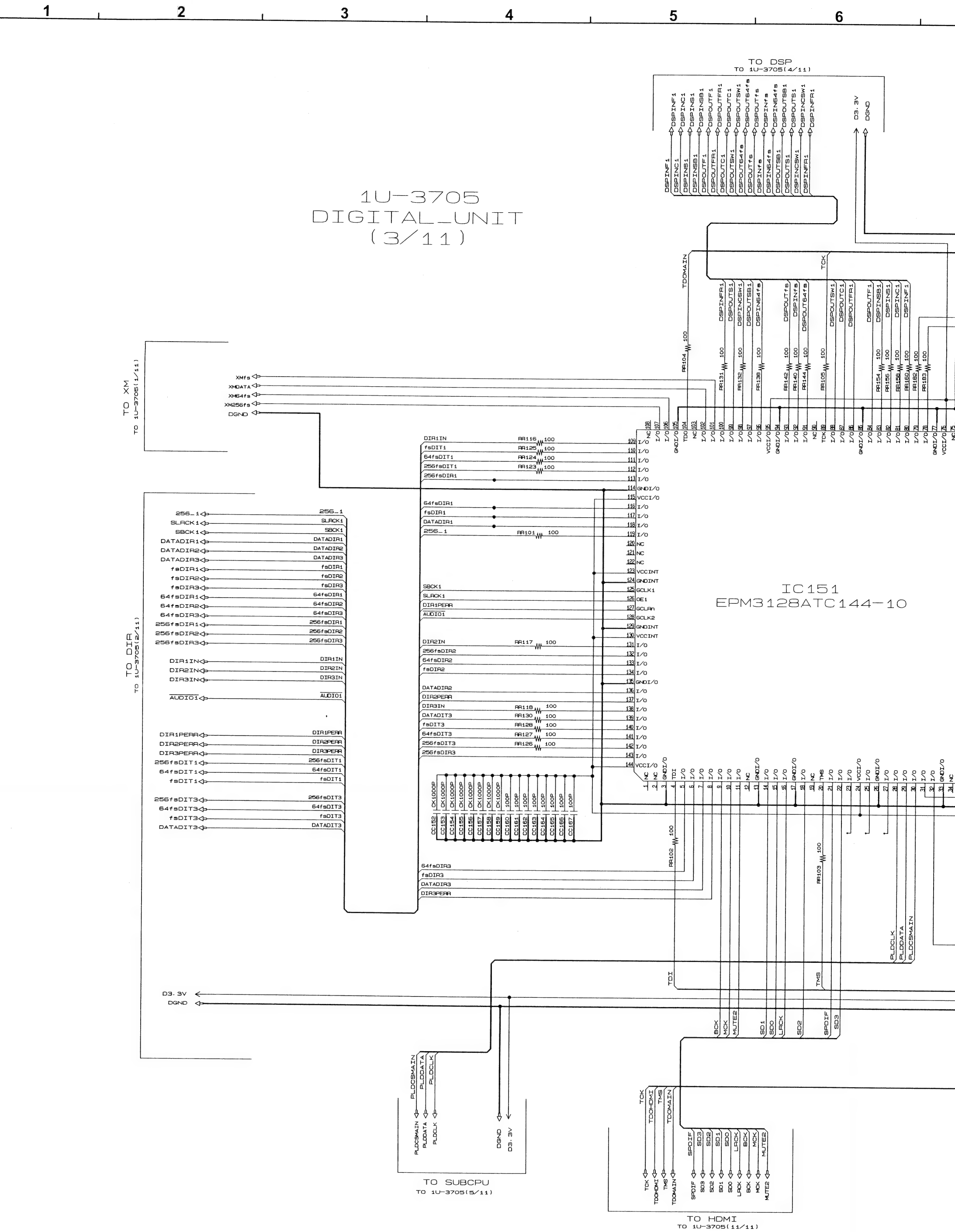


6 7 8 9 10 11



TO PLD
TO 1U-3705(3/11)

DIR SECTION (1/1)



1

2

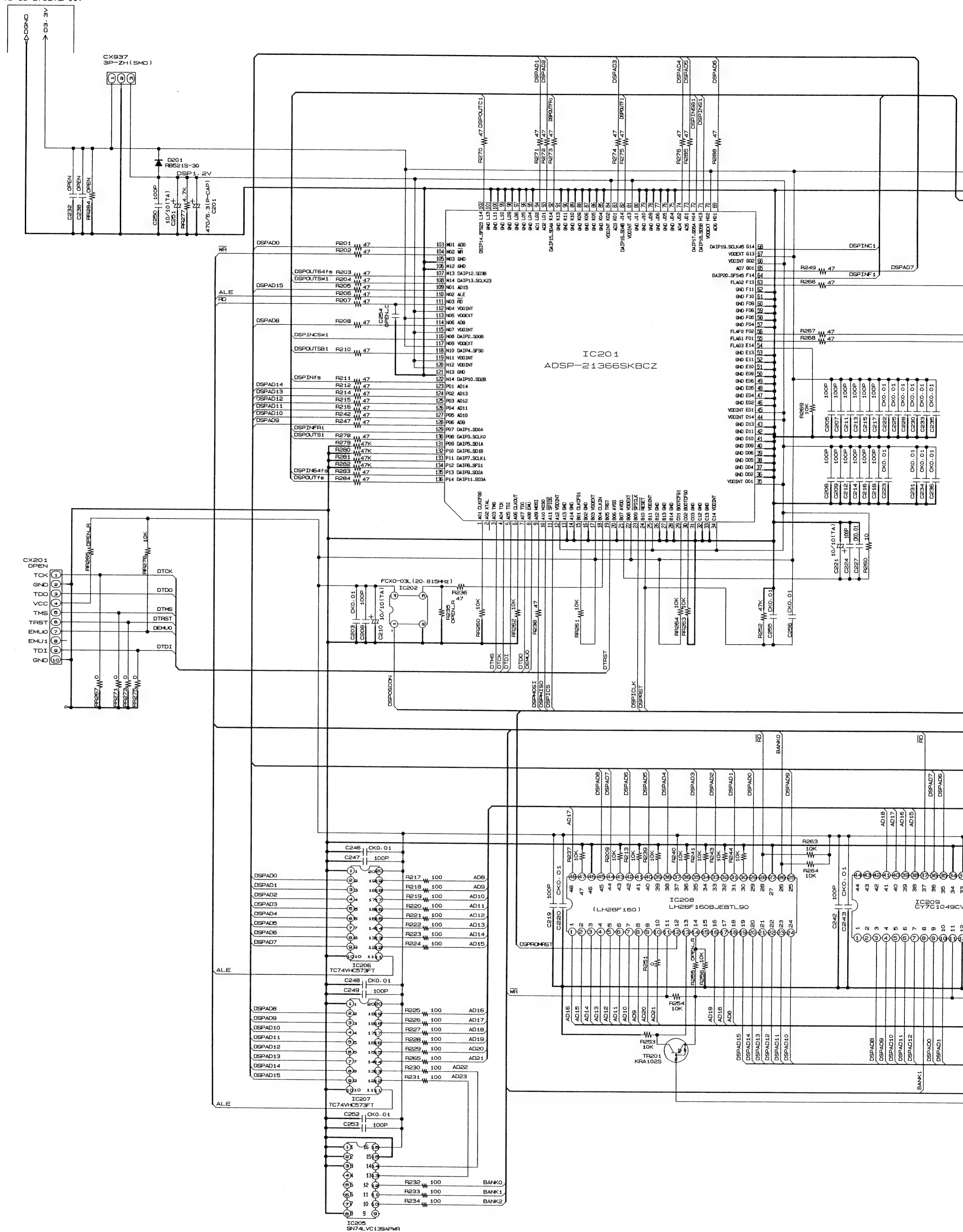
3

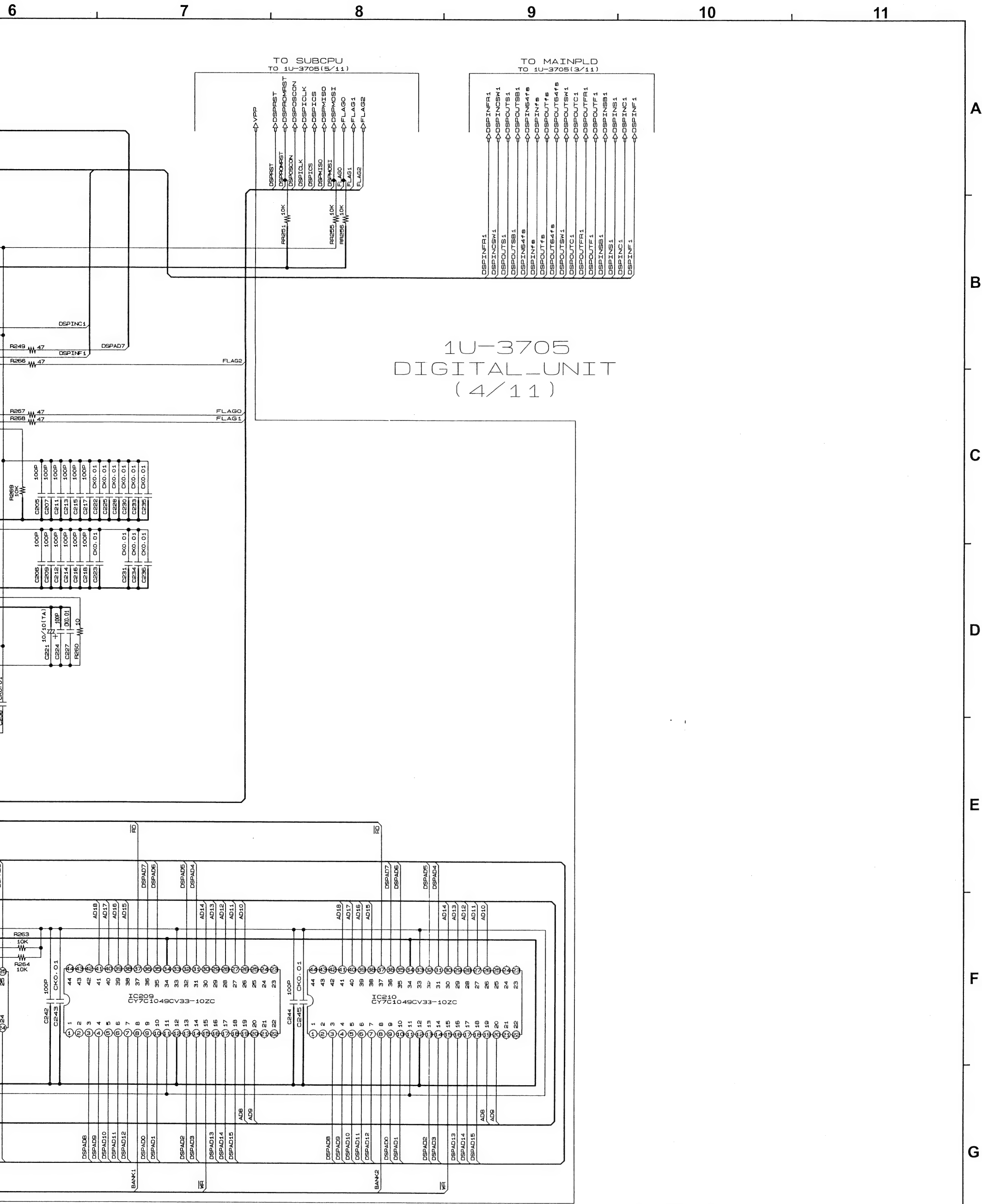
4

5

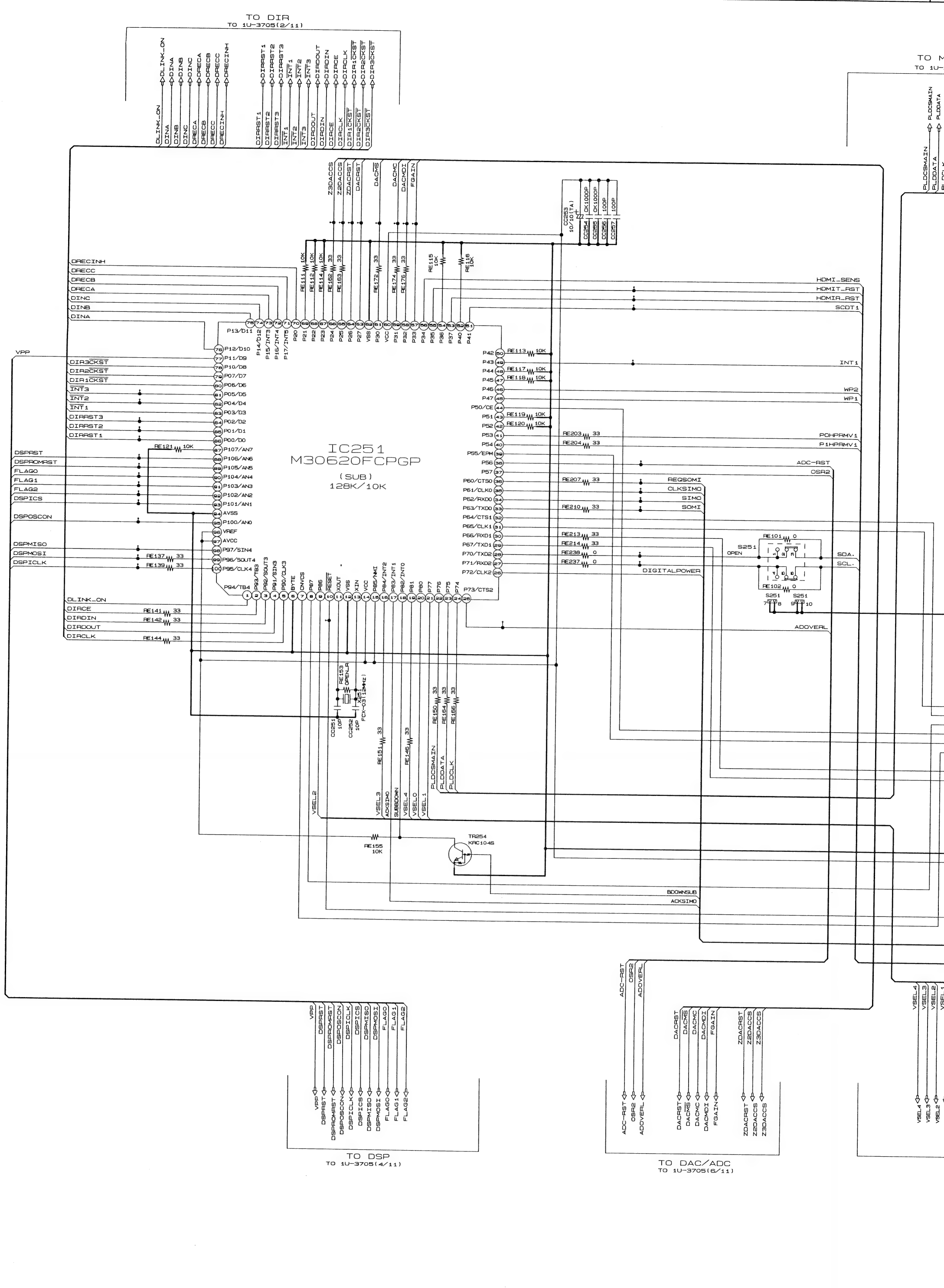
6

TO MAINPLD
TO 1U-3705(3/11)





DSP SECTION (1/1)



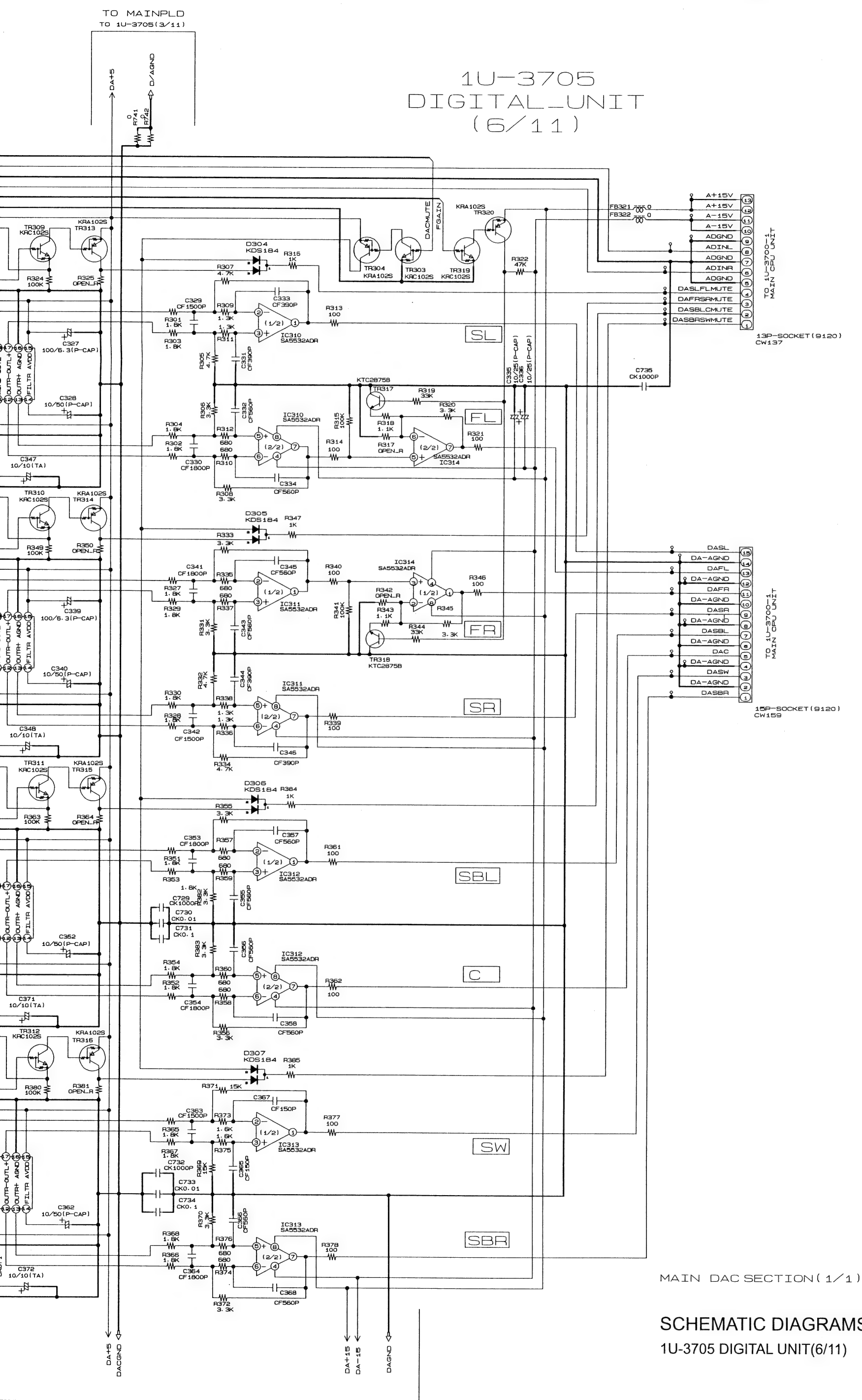


SCHEMATIC DIAGRAMS (25/31)
1U-3705 DIGITAL UNIT(5/11)



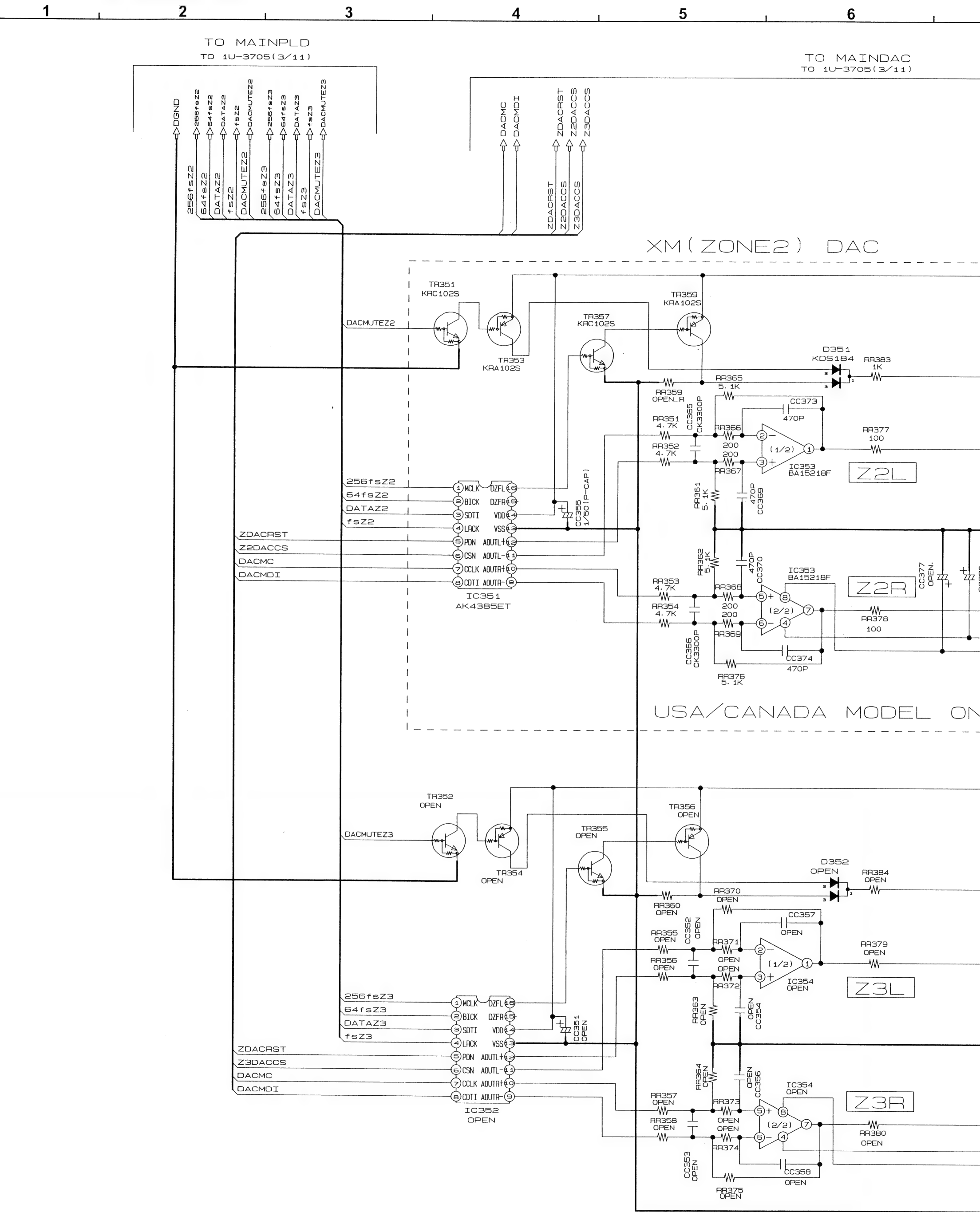
	PCM	DSD
1PIN	LRCK (fs)	DATA_R
2PIN	BCK (64fs)	GND
3PIN	DATA	DATA_L
4PIN	MUTE	GND
5PIN	SCK (256fs)	BCK (64fs)

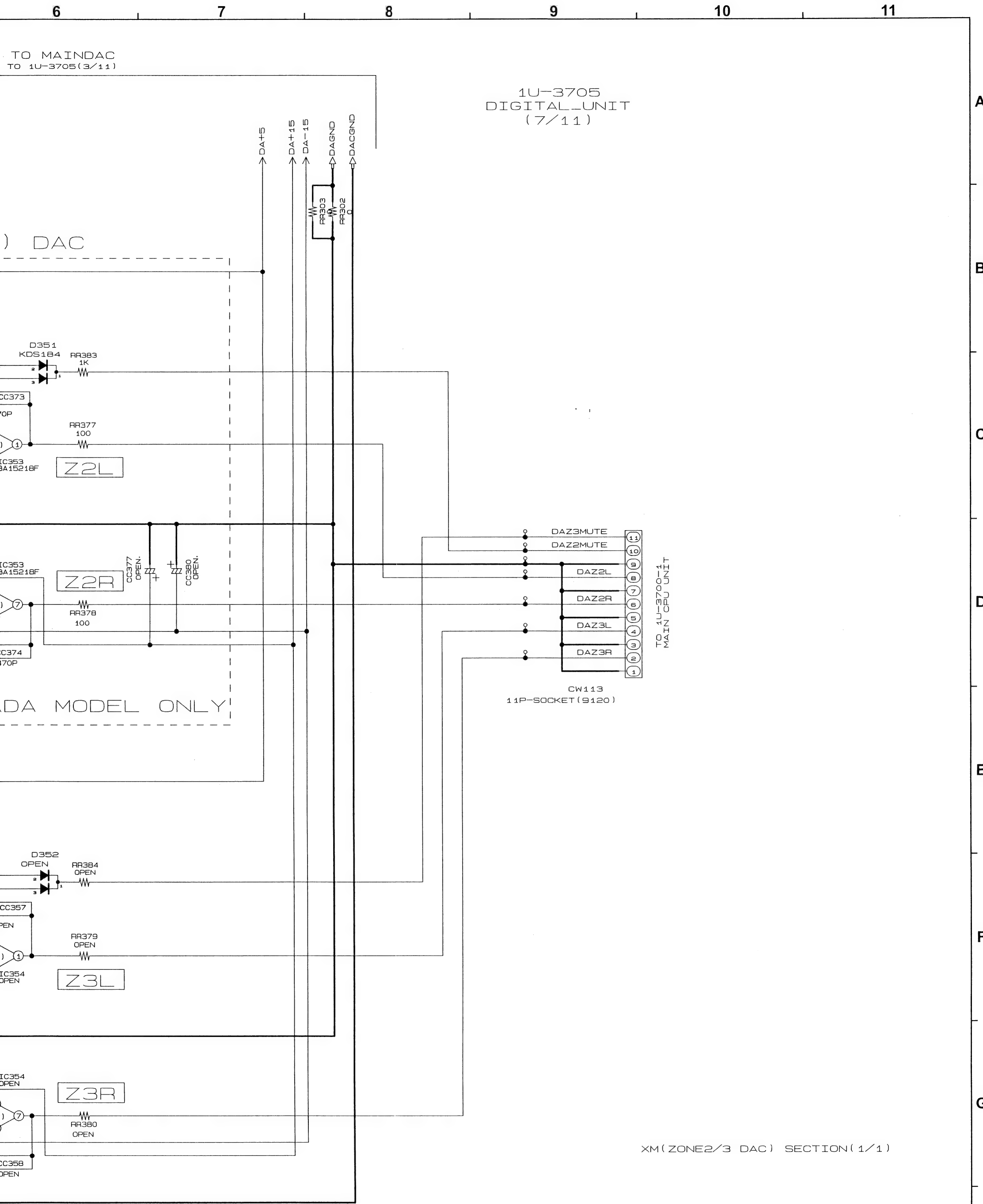
1U-3705
DIGITAL_UNIT
(6/11)



MAIN DAC SECTION(1/1)

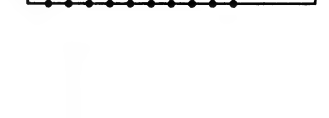
SCHEMATIC DIAGRAMS (26/31)
1U-3705 DIGITAL UNIT(6/11)

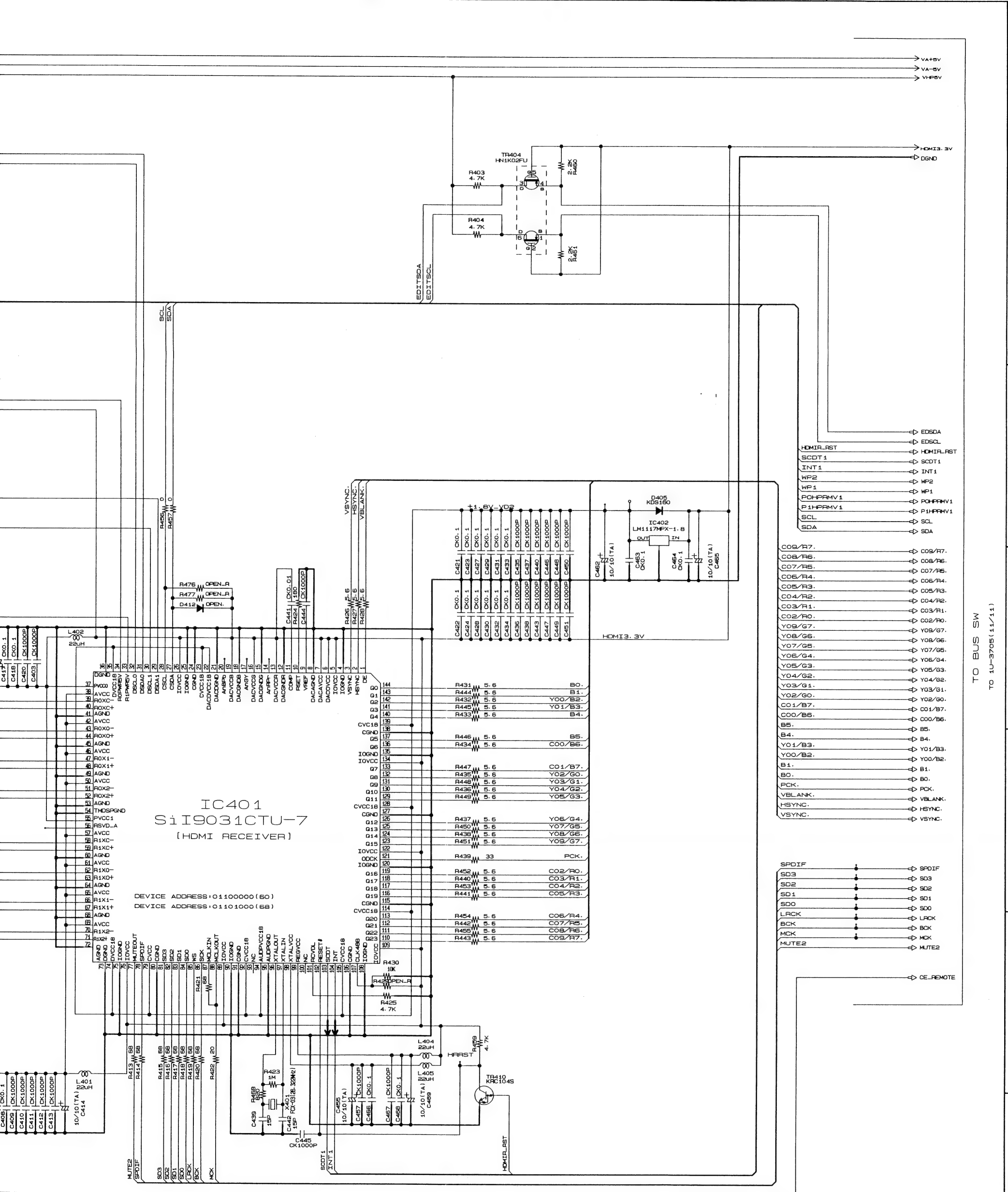




XM(ZONE2/3 DAC) SECTION(1/1)

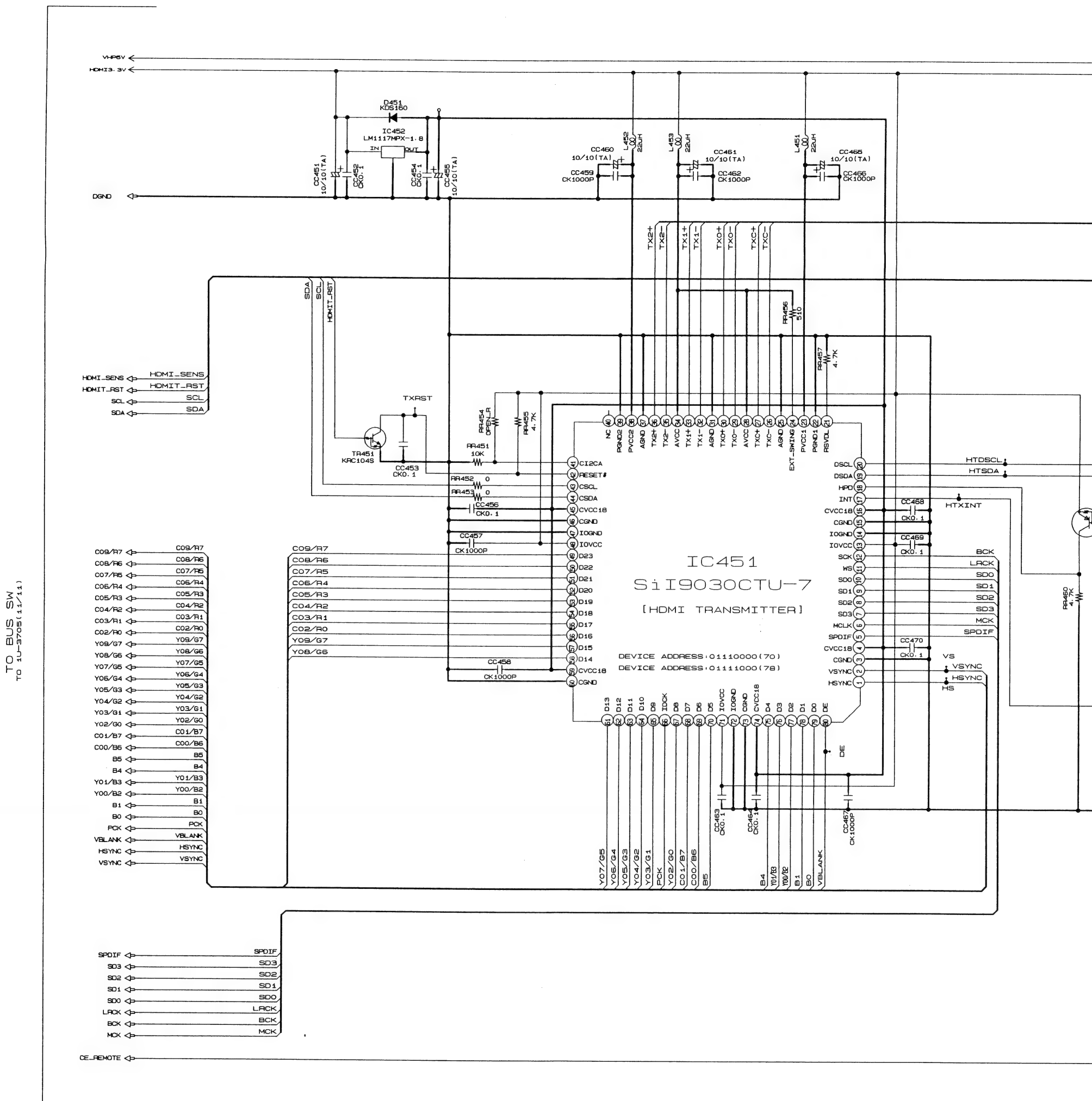
A horizontal number line with arrows at both ends. It has major tick marks labeled 1, 2, 3, 4, 5, and 6. There are also minor tick marks between each major tick mark, representing the midpoints of the intervals.





SCHEMATIC DIAGRAMS (28/31)
1U-3705 DIGITAL UNIT(8/11)

6



6

7

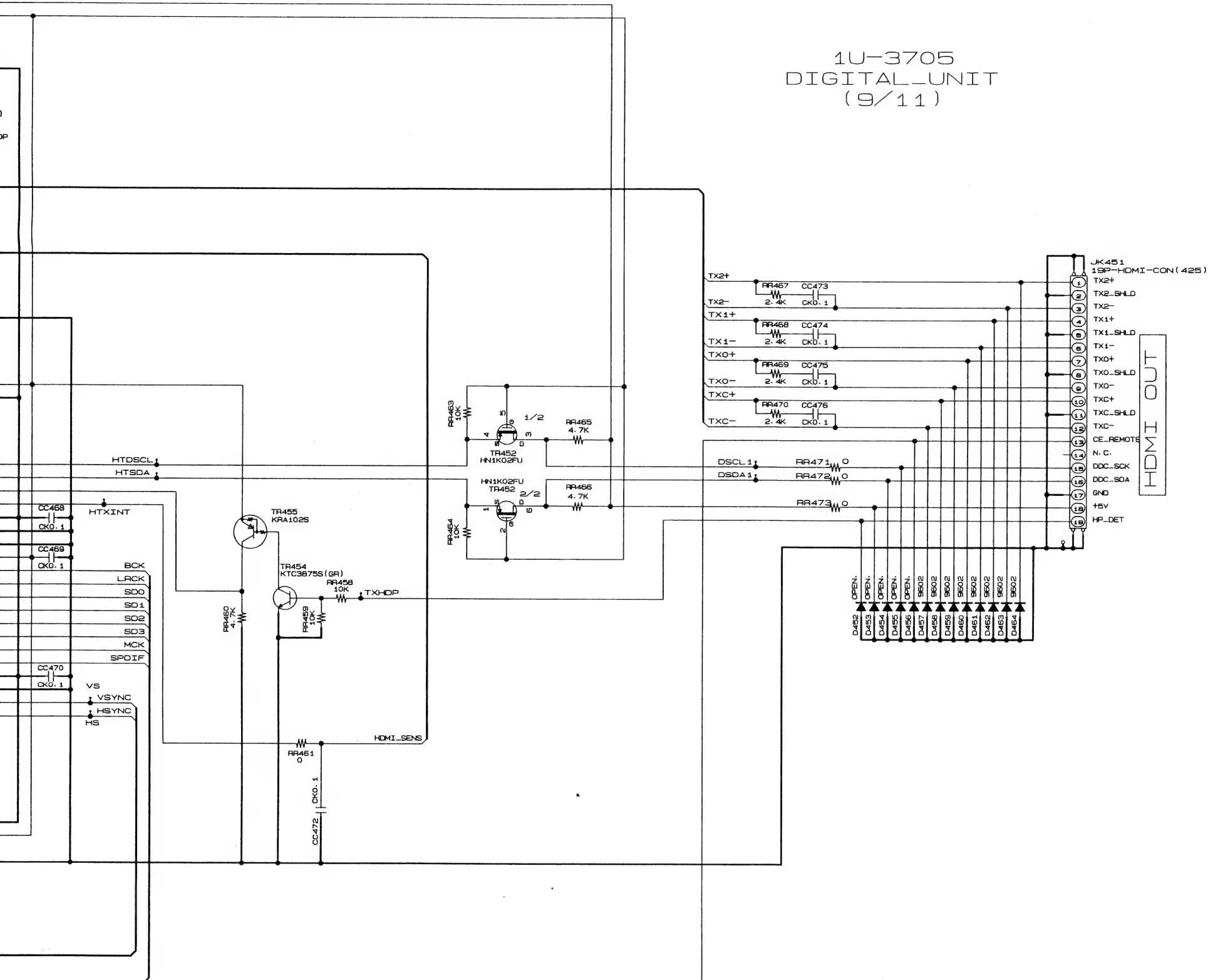
8

9

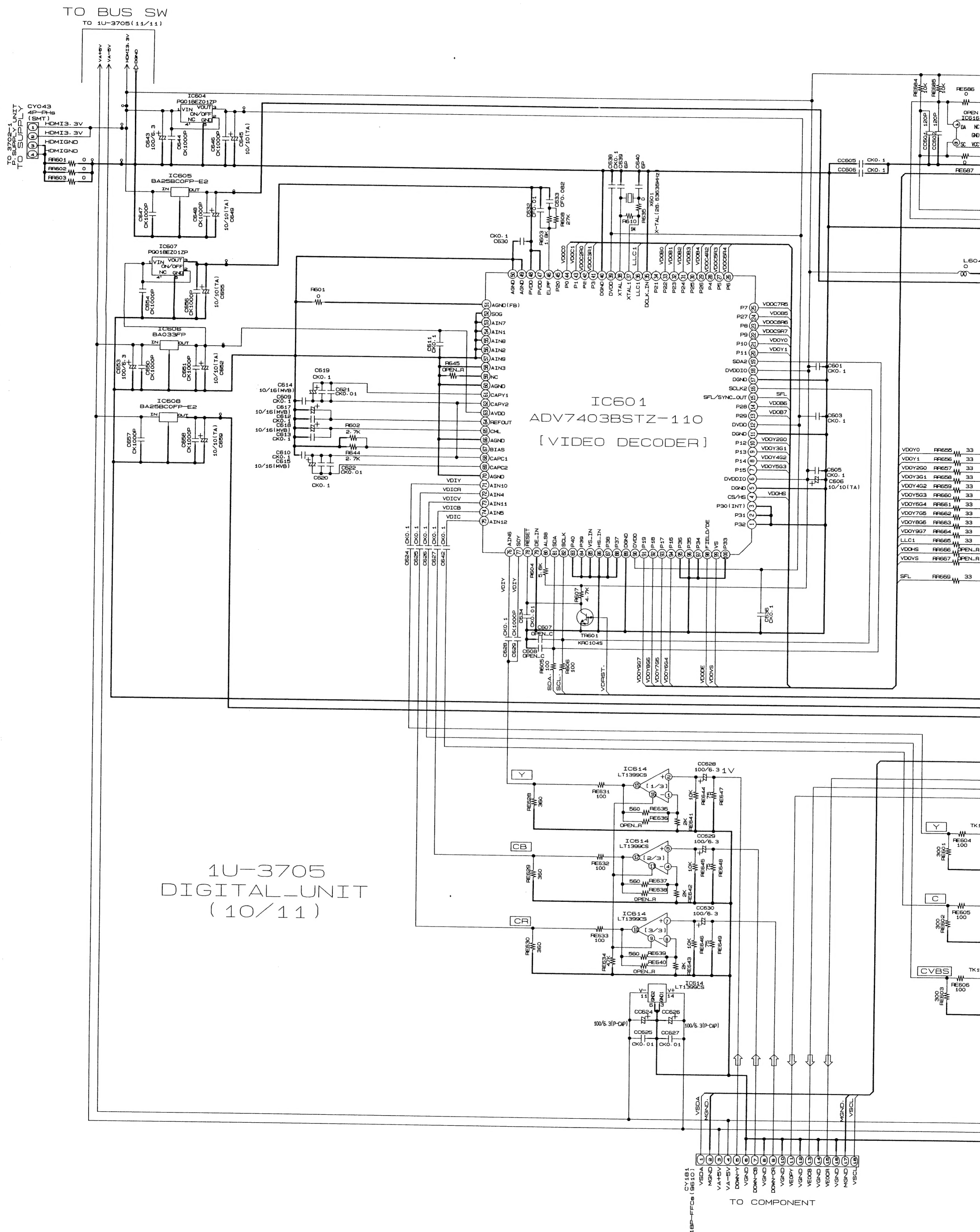
10

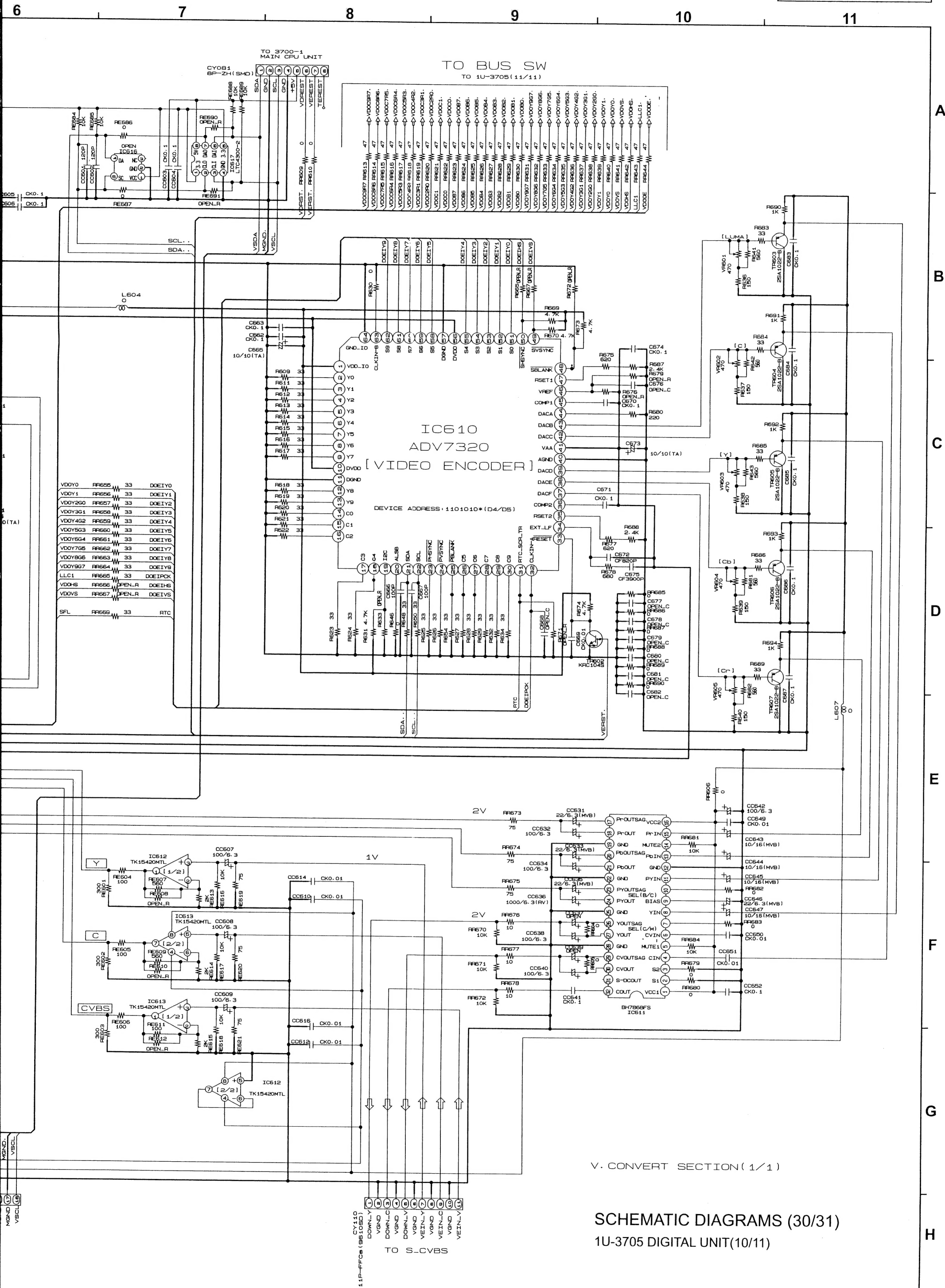
11

1U-3705
DIGITAL_UNIT
(9/11)



HDMI OUT SECTION(1/1)





1 **2** **3** **4** **5** **6**





SCHEMATIC DIAGRAMS (31/31)
1U-3705 DIGITAL UNIT(11/11)